FSM Research: Challenges, Opportunities & Current Thrusts in Wafer Manufacturing



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Fab Sort Manufacturing



Fab Dynamics

Factory is driven by Cost, Productivity, and Speed:

Reduce factory capital and operating costs per function

- Faster delivery of new and volume products to the end customer
- Efficient high volume production, high reliability, & high equipment reuse

Enable rapid process technology shrinks and wafer size changes



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Why do research in High Vol Mfg?

- 1 2th EMEA Academic Forum
- Moore's Law enabling significant technology development
- Manufacturing methods, techniques and innovation must keep pace with our technology roadmap
- Leveraging Universities for Manufacturing research allows us to explore fundamental and innovative concepts and theories



Developing Manufacturing Research Roadmap



Research collaboration models



STRJ, SEMATECH/SRC, etc.

(Coordination and Facilitization)

June , 2007 A single engagement model does not fit all scenarios

Research

Roadmap

Collaborate at

Internet speed

IC makers worldwide

(Requirements and Priorities)

Suppliers worldwide

(Capabilities, Potential Solutions)

(intel)

Research Methods

Seeking Improvement with a new Technology Applying a known

Applying a known technology to a problem

1) Engage members of the Research community to identify

•Technologies people using outside of Intel that we should be exploring?

• Current state of research on the technologies

•Informed estimates about the future of the technologies

2) Engage members of the Intel HVM organization to identify

- Targets of opportunity for application of the technologies
 - it may address a widely recognized problem
 - it may dramatically impact something not considered to be a problem

3) Engage members of the Intel HVM organization to identify

•The target problem, boundary conditions, assumptions, timing, etc

4) Engage members of the Research community to identify

- •The world-class experts on the problem you have specified
- The appropriate research arrangement with the experts

Cincer

ITRS FI 15 year roadmap

International Technology Roadmap for Semiconductor (ITRS) Factory Integration (FI) evaluates industry's difficult challenges and technology requirements to formulate potential solutions to meet these needs

Year of Production

Technology Node

X-Factor [1]

X-Factor [1]

Utilization

Availability

Wafer Diameter (mm

Non-hot lot (average of 94% lots)

Hot lot (average top 5% of lots)

Super hot lot (average top 1% o

Cycle time per mask layer (o High-mix capacity degradation

Bottleneck equipment [2] [3]

Wafer layers/day/head count Number of lots per carrier (high mix

Facilities cycle time (months 1st tool to 1st full loop wafer out

Floor space effectivenes

Node -to-node change-over (weeks)

Average number of wafers betwee reticle changes

Cycle time per mask layer (day

Cycle time per mask layer (days

2005

80

300

3.5 13

1×

1.

Develop FI challenges (world wide participation)

Difficult Challenges 252 nm	Summary Of Issues
Responding to rapidly changing, complex business requirements	Many new and co-existing business models including IDM, Foundry, Fabless, Joint Ventures, Collaborations, other Outsourcing, et: need to be considered in the Factory Integration
	Increased expectations by customers for faster delivery of new and volume products Need for improve integration of the entire product design and manufacturing process
	Faster design -> prototype and pilot -> volume production
	Enhanced customer visibility into outsourced production operations

Focus on reduction in cost per function (b) 30% per year [historic norms] & broductivity Improvement

Execus on delivering new/ compelling IC devices that continue our growth

		Increasing dedication of masks and equipment causing manufacturing inefficiencies
-		Challenges introduced with sharing of mask sets
		Difficulty in maintaining the historical 0.7× transistor shrink per year for die size and cost efficiency
	Managing ever increasing factory	Quickly and effectively integrating apid changes in process technologies
ιU	complexity	Managing carriers with multiple lots, wafers with multiple products, or multiple package form
1		factors
\sim		Comprehending increased purity requirements for process and materials
-	•	Need to run aluminum and copper backend in the same factory
\leq		Increasing number of processing steps coupled with process and product complexity
		Need to concurrently manage new and legacy software and systems with increasingly high interdependencies
		Explosive growth of data collection analysis requirements driven by process and modeling needs
		Increased requirements for high mix factories. Examples are complex process control as frequent recipe creation and changes at process tools and frequent quality control due to small to time:

Develop FI potential solution tables based on technology requirements

2012 2014 2016 DESISION SUPPORT TOOLS FO OPERATIONAL IMPROVEMEN Tools for understanding componer off between cycle time and equip Tools/Models/Algorithms: Tools/algorithms/models for wat based on bottleneck monitoring Analyze trade-off between CT and Utilization Tools/algorithms/models for f demand integration Tools to accurately predict and times to ensure On-Time-Delir > Optimize operator and technician staffing Tools/algorithms/models for op ➢ Predict and analyze lots CT to meet OTD staffing levels and developing or nlans Integrated techniques for coordination Coordination with SCM management efforts (fab/sort/assy/tes Tools for determining optimum quantities operations resources (such as Reticle and Tools for determining optimal Non-product wafer Tools/alcorithms/models to measure actual capacity los components due to operators and technicians



Multiple

1×

Multiple

4.

Provide direction for the semiconductor

Drive direction for industry research, pre-competitive standards and industry enabling capabilities

Learn from each other (ex. High Mix production, requirements)

Influence Supplier Roadmap (common industry requirements, drive potential solutions, set time table for solutions)

Influence other IC Maker Direction (reduce re-invention, pool resources, increase reuse for cost reduction (standards))

Understand our competitive position in the industry and where we need to drive changes

Show Industry Leadership & Recruit students

Source: International Technology Roadma for Semiconductors

Some Projected Attributes of a 300mm < 45nm Fab





Factory Integration Requirement Drivers

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Cycle Time Reduction & Operational flexibility

More good wafers out per tool

Highly automated factory

Reduce Time to Money

Factory size is becoming an issue

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Source: ITRS

Cycle Time/Operational Flexibility: Multiple lots per carrier and/or fewer wafers per carrier. Get new products to customer much faster.

Output per tool must increase: Find breakthrough solutions that result in significant increases in good wafer out and increased OEE (eg: APC, e-Diag)

The 300mm factory is much more <u>automated</u> and must be designed to transport hot-lots and hand-carry's.

Reduce time to \$\$\$/Cycle-time reduction: What are stretch goals for cycle time from ground-breaking to first full loop wafer out. How to achieve quicker shrink?

Increased floor space effectiveness: Don't want each new generation to drive big increase in cleanroom size, esp. since fab is segregated Cu/non-Cu and new metal layers added at each node.

Source: International Technology Roadmap for Semiconductors

FI Technology Requirements



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Source: ITRS

Data Convergence Vision

The correct data needed by the user to make an informed decision are available at any

time both in-situ and remotely

Demand

"Data is pre-processed at each level based on job role requirements using in-line intelligence to ensure an optimised decision making process" Any Time, Anywhere, Any Device

Process Data Sources



Building the Data Ecosystem

Yield Data

Excursion Prevention

Advanced Sensors

Data Fusion

APC Frameworks

"... The excess of data often impedes the ability of the process engineer to interpret data and analyse the data. This problem requires the development of new data mining applications..."

Tool Utilisation

FDC Data

Tool Availability

"...Data fusion is a paradigm for integrating data from multiple sources to synthesise new information such that the whole is greater than the sum of its parts..."¹

¹ 'Lyengar et al 'Foundations of Data Fusion for Automation' IEEE Instrumentation & Measurement Magazine Dec. 2003

The Data Explosion



Process Technology Generation

Faced with an explosion in the amount of data being generated the standard approach will no longer work.



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Data Volume

Development Focus



Applications & Human interfaces

Business processes And methodologies

Data & Information Systems

Information & Technology services Highly integrated data environments of both structure and unstructured data sets based on standard data formats and data exchange protocols. Increased speed and quality of decision making. Proactive decision making rather than reactive.



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Areas of Focus

Production Equipment

- Process and Metrology equipment
- Mainframe and process chambers
- Wafer Handling Robots, Load Ports
- Internal software & computers

Factory Operations

- Policies & procedures used to plan, monitor and control production
- Direct factory labor

Factory Information & Control

- Data and Control systems required to run the factory
- Equipment and Process Control Systems
- WIP Tracking and Decision support Systems
- Plan, Schedule, Dispatch Systems
- Computers, databases, software outside equipment



List of Potential Research Areas

3D (Multiple-floor) L/O

Real-time event-driven scheduling

Lot streaming (single wafer processing)

Queuing theory application for CT estimation (CT modeling)

CT reduction:

- a) Variability impact and elimination
- b) WIP bubble forecasting and recovery

c) Design For Manufacturability (DFM) and TPT-Yield trade-offs
 Demand/Data Flow Technology (DFT) implementation
 Capacity Planning/Modeling: the impact of multiple constraints
 Automated tool-L/O planning



Iceberg model – Predictive Maintenance

A failure is just the tip of the iceberg!

Our Challenge: Find hidden signals from our existing data sources to predict failures.

Failures

Hidden signs



Predictive maintenance - opportunity



Eliminate equipment variability by moving unscheduled events to scheduled.

Increased availability by extending time between PM cycles. Perform maintenance JIT, better resource balancing.

Reduced parts replacement = \$.

Caution: Need to ensure no Yield impact or CT.



Predictive Maintenance decision parameters



Predictive Maintenance vision.....



Standalone proc

Standalone process data sources fused into factory data warehouse. Facilitate Predictive maintenance analytics.

Factory equipment simulation model integrates with Fused data.

Predictive Maintenance algorithms integrated with simulation data, generate PM schedules & track parts degradation.



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Summary

We have four unique production advantages:

- Technology Pipeline
 Agility
- Scale

• Operational Excellence

We're extending Moore's Law with industry-leading technology We are enhancing our Manufacturing capabilities through innovation and integration of our core production advantages