

12th EMEA Academic Forum

Budapest June 12-14, 2007

Welcome !



We are honored !

- For the Hungarian Government to have taken on the Patronage of this 12th EMEA Academic Forum
- To be hosted by the Budapest Tech (Budapesti Műszaki Főiskola) or BMF

Thank You !



Ground Rules / Some Information

- **NO SMOKING**
- **Mobile phones OFF or on vibrate**
- **Keynotes are constructed to have about 40% Q&A at the end**



The 12th EMEA Academic Forum Booklet



Academic Forum




Intel® 12th EMEA
Academic Forum
June 12 to 14, 2007
Budapest, Hungary



www.intel.com/education/highered



Locations



These are the rooms where the 12th Intel EMEA Academic Forum takes place

Room name	Location	Sessions held
Auditorium Maximum	Ground Floor	Forum Opening, Plenary Sessions and Split Sessions "Systems & Architecture"
Lecture Room F.09	Ground Floor	Split sessions "Silicon Technologies & Manufacturing Track"
Foyer Lecture Room F. 09	Ground Floor	Coffee, drink and snack buffet
Hall 1	Ground Floor	Lunch
Hall 2	1 st Floor	Poster Sessions

Press rooms (June 12 only)

Room 4.01	1 st floor	Interviews
Room 2.02	2 nd floor	Waiting Section



The Agenda



Intel® 12th EMEA Academic Forum



12 th Academic Forum in Budapest 12.06.07						
Start	End	Duration	Activity	Presenter		
9:00	9:20	0:20	Welcome and Introduction	Prof. Dr. h.c. Mark Harris, Director EU Higher Education and Research	Forum Opening	
9:20	9:30	0:10	Local welcome	Prof. Imre J. Rudas, Rector of Budapest Tech		
9:30	9:40	0:10	Welcome Video	Christian Morales, Vice President of Intel Sales & Marketing Group, General Manager, Intel EMEA		
9:40	10:25	0:45	...the Next Billion...	William Swope, Corporate Vice President & Director, Intel Corporate Affairs Group		
10:25	11:00	0:35	Break			
11:00	11:30	0:30	Ministry of Economy and Transport	Deputy Minister Abel Garamhegyi	Plenary Sessions	
11:30	12:30	1:00	Keynote	Joseph Schutz, Vice President, Corporate Technology Group Director, Microprocessor Technology Lab		
12:30	13:30	1:00	Beyond Cores	Steve Pawlowski, Intel Senior Fellow Digital Enterprise Group Chief Technology Officer, Digital Enterprise Group and General Manager, Architecture and Planning		



The Agenda

13:30	14:30	1:00	Lunch		
14:30	15:30	1:00	Policy & Innovation – The Role of Academia	Donald Whiteside, Vice President, Corporate Technology Group, Director, Technology, Policy & Standards	Plenary Sessions
15:30	16:30	1:00	Intel's Silicon Technology R&D Pipeline	Dr. Ian Young, Intel Senior Fellow, Technology and Manufacturing Group; Director, Advanced Circuits and Technology Integration	
16:30	18:00	1:30	Gender Diversity Panel: Advancing Women in Science and Engineering	Dr. Martina Roth, Intel EMEA Education Director	
18:00	19:30	1:30	Poster Session #1		
19:30	19:40	0:10	Bus Pickup		
19:40	22:30	2:50	Evening Event	Zsófia Főhercegné (Danube Ship) Petöfi tér 9. számú kikötő (Dock No. 9.)	

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The Agenda



Intel® 12th EMEA Academic Forum



12th Academic Forum in Budapest 13.06.07					
Morning plenary Sessions					
Start	End	Duration	Activity	Presenter	
9:00	10:00	1:00	Network Security: New Essentials for Safeguarding Network Systems	Dr. Kevin Kahn, Intel Senior Fellow, Corporate Technology Group Director, Communications Technology Lab	
10:00	11:00	1:00	Technology Challenges and Opportunities for the 21 st Century	Gene Meieran, Intel Senior Fellow, Technology and Manufacturing Group Director, Manufacturing Strategic Support	
11:00	11:15	0:15	Break / track change		

Start of dual track agendas						
			Systems & Architecture Track		Silicon Technologies & Manufacturing Track	
Start	End	Duration	Presentation	Presenter	Presentation	Presenter
11:15	12:00	0:45	Technical Overview of the 45nm Next Generation Intel® Core™ Microarchitecture (Penryn)	Ronny Ronen, Senior Principal Engineer, Corporate Technology Group	And scaling goes on... Technology Challenges & Opportunities	Dr. Jose Maiz, Intel Fellow, Technology and Manufacturing Group Director, Logic Technology Quality and Reliability
12:00	12:45	0:45	Codesigned Virtual Machines: New Opportunities in Processor Architecture	Dr. Antonio Gonzalez, Founding Director of the Intel Barcelona Research Centre	Tops Down Nanotechnology Breakthrough	Paula Goldschmidt, Principal Engineer and Director of Technology Development, Technology and Manufacturing Engineering

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The Agenda

12:45	14:00	1:15	Lunch			
14:00	14:45	0:45	Multicore: The Software View	James Cownie, Principal Engineer	Materials & Metrology Challenges In Planarization and Interconnect Technologies	Dr. Mansour Moinpour, Principal Engineer and Materials Technology & Engineering Manager Materials Organisation
14:45	15:15	0:45	Break			
15:15	16:00	0:45	Dynamic Binary Instrumentation and Tools for Supporting Multi-threaded Applications	Moshe Bach, Intel Corporation Israel Design Center Software Solutions Groupz	Synthesis of Operational Excellence and Research in HVM: Intel Manufacturing's Story of Change & Opportunity	Gopal Rao, Director, Fab Sort Manufacturing Research
16:00	17:30	1:30	Poster session			
17:30	18:00	0:30	Bus Pickup			
18:00	22:30	4:30	Evening Event	Budapest Historical Museum Baroque Hall 1. Szent György ter 2		



The Agenda



Intel® 12th EMEA Academic Forum

12th Academic Forum in Budapest 14.06.07						
Dual Track Agenda - Continue						
			Systems & Architecture Track		Silicon Technologies & Manufacturing Track	
Start	End	Duration	Presentation	Presenter	Presentation	Presenter
9:00	9:45	0:45	Bandwidth and Latency Challenges for Multi-Core Server Performance	Dr. Fayé A. Briggs, PhD Chief Performance Architect, Digital Enterprise Group, Intel Corporation	Novel Interconnect Characterization Techniques	Dr. Scott List, Principal Engineer
9:45	10:30	0:45	Technical Presentation	Noel Murphy	The measurement and control of process variation in High Volume Manufacturing semiconductor Fabs	Niall MacGearailt, Research Engineer
10:30	11:00	0:30	Break			
11:00	11:45	0:45	FIGH: Weaving together the threads of biomarkers, bio-informatics and behavioral markers to understand diseases	Dr. Terrance J. Dishongh, Intel Corporation	IC materials for the next decade 450mm challenges and opportunities	Dr. Michael Goldstein, Principal Engineer
11:45	12:15	0:30	Wrap Up			
12:15	13:15	1:00	Lunch			
12:15	12:45	0:30	Transfer to Mercure Hotel	for Multi-Core Workshop participants only!		
12:45	13:30	0:45	Lunch at Mercure Hotel	for Multi-Core Workshop participants only!		



The Poster Sessions

Poster Sessions on June 12, 18:00 - 19:30 h

Academy of Economic Studies, Bucharest, Romania
and Awway Software, Paris, France

Booth 1

Julian Ilie-Nemedi and Iulia Surugin

- Title 1 - eGovernment Advanced Information Service Bus (eGov-Bus)
- Title 2 - eBusiness Security (EBS)
- Title 3 - Integrated System for Mobile Users (SIUM)
- Title 4 - AJAX RPC Framework (AJAX-RPC)
- Title 5 - WS-Eventing Framework for WCF (WS-E4WCF)

Academy of Economic Studies Bucharest

Booth 2

Radu Constantinescu

- Title 1 - E-Business Security
- Title 2 - Improving organizations efficiency through CMMI

Alexandria University

Booth 3

Prof. Mohamed Rizk

- Title 1 - Hardware implementation of ADS on FPGA
- Title 2 - VLSI implementation of neural networks for image compression and face recognition
- Title 3 - Routing of sensor networks
- Title 4 - Multipath routing in Adhoc networks

Bar Ilan University, Israel

Booth 4

Prof. Zehavi Ephraim

- Title 1 - Spectral Methods in VLSI CAD
- Title 2 - Optimization of wireless resource Allocation using Game Theory

Bar-Ilan University

Booth 5

Prof. Moschytz George

- Title 1 - Game-Theory Applications for Resource Allocation
- Title 2 - Spectral Decomposition Methods for VLSI CAD

Ben-Gurion University

Booth 6

Prof. Herzl Aharoni

- Title 1 - Two Terminal Single Crystal Silicon Integrated Light Emitting Devices
- Title 2 - Multi Terminal Single Crystal Silicon Integrated Light Emitting Devices

Bilkent University

Booth 7

Prof. Cevdet Aykanat

- Title 1 - Efficient Parallel Text Retrieval and Query Processing
- Title 2 - SEE-GRID: South Eastern European GRID Enabled eInfrastructure Development: A Grid-Enabled Search Engine
- Title 3 - Parallel Direct Volume Rendering
- Title 4 - Parallel Web Crawling, Categorization, and PageRank Computation
- Title 5 - Parallel Preconditioned Iterative Methods

BME

Booth 8

Dr. Andras Poppe

- Title 1 - Circuit design for ambient intelligent applications
- Title 2 - Circuit designs for biomedical applications

Bogazici University

Booth 9

Prof. Delic Hakan

- Title 1 - Robust UWB Transceiver Design
- Title 2 - Bogazici University Wireless Communication and Networking Curriculum



Intel® 12th EMEA Academic Forum



The Dinner Venues



Intel® 12th EMEA Academic Forum



Table Allocation Evening Event June 12

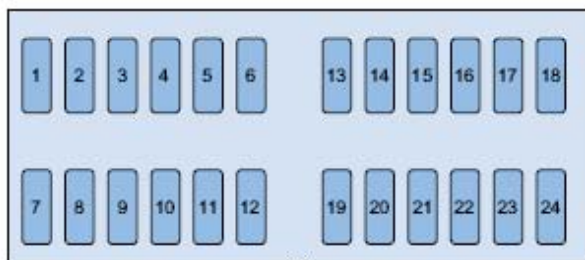
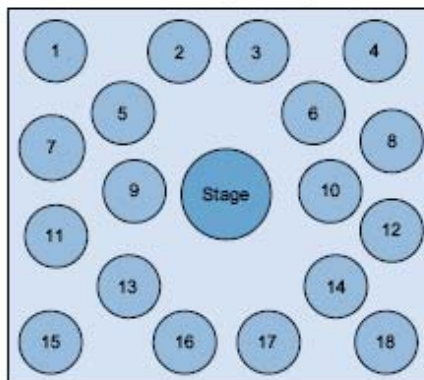


Table Allocation Evening Event June 13



Name	Table Number June 12	Table Number June 13
Dr. Moshe Bach	20	9
Faye Briggs	21	10
Jim Cownie	1	13
Paula Goldschmidt	2	14
Dr. Mike Goldstein	3	16
Prof. Antonio Gonzales	16	n/a
Prof. Dr. h. c. Mark Harris	6	2
Dr. Kevin Kahn	5	3
Yossi Levy	17	17
Dr. Scott List	18	7
Niall MacGearalt	7	11
Dr. Jose Maiz	8	8
Dr. Gene Meieran	4	5
Dr. Mansour Moinpour	9	12
Noel Murphy	11	1
Jenny Patterson	15	4
Steve Pawlowski	13	n/a
Gopal Rao	22	15
Ronny Ronen	14	n/a
Dr. Martina Roth	6	2
Joseph Schutz	10	n/a
Will Swope	6	n/a
Donald Whiteside	12	n/a
Dr. Ian Young	19	6



Venues, Busses, Important Phone Numbers

Conference Venue

Budapest Tech
John von Neumann Faculty of Informatics
Bécsi út 96/b
HU - 1034 Budapest

Hotel and venue for Workshop June 14 to 16

Mercure Budapest Korona****
Kecskemeti Street 14
Telephone +36 1/4868800
Telefax +36 1/3183867
Email H1765@accor.com

Evening Event June 12

Zsófia Főhercegné (Danube Ship)
Petőfi tér 9. számú kikötő (Dock No. 9)

Evening Event June 13

Budapest Historical Museum (BHM)
Baroque Hall
I. Szent György ter 2
www.btm.hu

Important Phone Numbers

Ambulance 104
Police 107
Airport (+36-1)-296-9696
Taxi (city) (+36-1)-2-333-3333
Taxi (airport) (+36-1)-365-5555

Event Manager

Sabine Stark +49/171/8187511

Bus transportation	from	to	Departure time
June 12	Mercure	Budapest Tech	8:00
	Budapest Tech	Zsofia	19:30
	Zsofia	Mercure	22:30
June 13	Mercure	Budapest Tech	8:20
	Budapest Tech	BHM	17:30
	BHM	Mercure	22:30
June 14	Mercure	Budapest Tech	8:20
	Budapest Tech	Mercure	12:30
	Budapest Tech	Mercure	13:30
	Budapest Tech	Mercure	14:00

Please note that - if you miss the transfer bus - you will have to arrange a private transfer.



WIRELESS ACCESS

The name of the network: BMF-hotspot

Username: intelemea

Password: emeaintel



Thank You !

Have a Great Forum !

