



Intel® Xeon® Processor 7500 Series Thermal Model Overview

Legal Disclaimer

Notice: This document contains information on products in the design phase of development. The information here is subject to change without notice. Do not finalize a design with this information. Contact your local Intel sales office or your distributor to obtain the latest specification before placing your product order.

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY RELATING TO SALE AND/OR USE OF INTEL PRODUCTS, INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT, OR OTHER INTELLECTUAL PROPERTY RIGHT. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications, product descriptions, and plans at any time, without notice.

All products, dates, and figures are preliminary for planning purposes and are subject to change without notice.

Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them.

The Intel Xeon Processor 7500 Series thermal models may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Code names presented in this document is only for use by Intel to identify a product, technology, or service in development, that has not been made commercially available to the public, i.e., announced, launched or shipped. It is not a "commercial" name for products or services and is not intended to function as a trademark.

Copies of documents which have an order number and are referenced in this document, or other Intel literature may be obtained by calling 1-800-548-4725 or by visiting Intel's website at <http://www.intel.com>.

Intel, Xeon, and the Intel logo are trademarks of Intel Corporation in the U.S. and other countries.

*Other names and brands may be claimed as the property of others.

Copyright © 2010, Intel Corporation. All Rights Reserved.



Summary

Objective: Provide heatsink thermal CFD models to customers to assist in their system and component thermal evaluations. Values based on CFD and compared to test results.

Heatsink Size and Boundary Conditions

Heatsink Form Factor	Overall HS Size (w x l x height) mm	TDP W	Tamb C	Airflow CFM	T rise C	Delta_P IN H2O
Preliminary - subject to change						
1U/blade proxy utilizing existing retention	90 x 90 x 26.5	95.0	35.0	12.6	7.0	0.34
2U utilizing existing retention Cu base / Al fins	90 x 90 x 51.0	105.0	35.0	28.0	7.0	0.25
Enabled solution: 4U tower 100mm wide	100 x 70 x 102.5	130.0	35.0	36.0	10.0	0.20

Non-Uniform Correction Factors

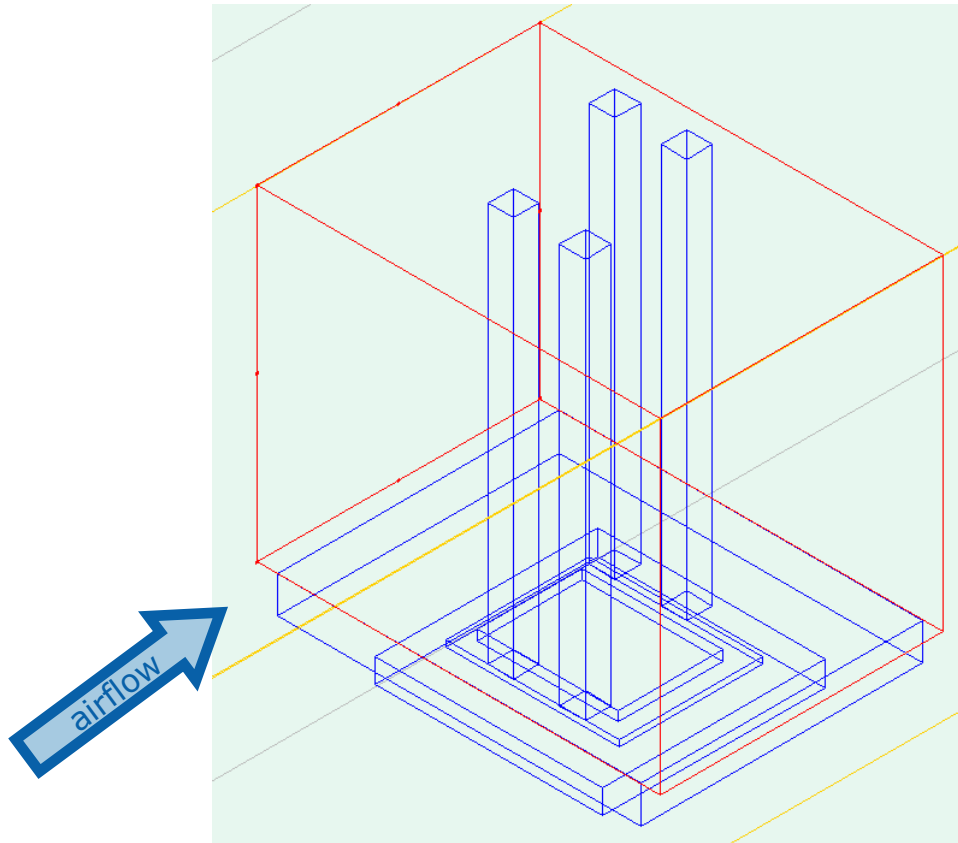
Correction Factor (CF) is added to the Thermal Test Vehicle (TTV) Ψ_{CA} heatsink to account for power non-uniformity and any difference in die size between TTV and product.

$$\text{Processor } \Psi_{CA} (\text{non-uniform}) = \text{TTV } \Psi_{CA} (\text{uniform}) + \text{TTV CF}$$

TDP(W)	TTV CF (°C/W)
130	-0.018 °C/W
105	-0.017 °C/W
95	-0.017 °C/W

Thermal correction factor values are subject to change.

Tower Heatsink – Flotherm Compact Model



HS overall: 100 x 70 x 102.5 mm (max)

Fin volume: 100 x 70 x 80.8 mm

HS pedestal: 35.0 x 26.0 x 2.5 mm

4 L-shaped heatpipes

Represents: 64 fins @ 0.3mm thick, Al

Note: Cu pedestal required to clear ILM.

This compact model to be utilized in the system model to:

- obtain airflow through CPU fins to determine spec compliance utilizing the performance to airflow graph/equation
- represent the CPU stackup resistance and power dissipation in the system

Enabled Tower Heatsink Performance @ 36 CFM

	Enabled Heatsink w/ Xeon® 7500	Enabled Heatsink w/ Westmere-EX	Note
TTV Ψ_{CA} Mean EOLife	0.184	0.184	TTV and heatsink are assumed same for Intel Xeon processor 7500 series and Westmere-EX processor
3σ	0.018	0.018	
Ψ_{CA} TTV CF	-0.018	TBD	Based on silicon powermaps Westmere-EX processor TBD
Ψ_{CA} NU, EOLife + 3σ	0.184	TBD	Xeon 7500 Requirement: Ψ_{ca} max = 0.185 C/W for 130W TDP, T_c -max = 69C, T_{la} = 45C
Pressure Drop (IN H2O)	0.19	0.19	Spec limit = 0.20

Performance vs. Airflow

