



Declaration of Conformity

Equipment type: **Boxed Quad-Core Intel® Xeon® Processor 3200 Series**

Product codes: **BX80562X3210 SL9UQ, BX80562X3220 SL9UP**

The equipment described above is declared to be in conformity with the following applicable national and international standards, when tested in a representative chassis. The conformity is valid only when the equipment is used in a manner consistent with the manufacturer's recommendations and the reference documents.

Document no. / Edition / Date of issue	Title
<i>EMC Class B:</i>	
EN 55022:1998 + A1:2000 + A2:2003	Information Technology Equipment – Radio disturbance characteristics – Limits and methods of measurement
EN 55024:1998 + A1:2001 + A2:2003	Information Technology Equipment – Immunity Characteristics – Limits and methods of measurement
<i>Safety/Low Voltage:</i>	
EN 60950-1: 2001	Safety of Information Technology Equipment – Part 1: General Requirements

Additional information:

EMC Testing Lab

Northwest EMC, Inc.
22975 NW Evergreen Parkway
Hillsboro, OR 97124 USA

Safety Testing Lab

Underwriters Laboratories, Inc.
2600 NW Lake Road
Camas, WA 98607 USA

Regions for which conformity is declared:

European Economic Area (EEA): Intel Corporation declares the equipment in compliance with the essential requirements of EC Council Directives 73/23/EEC (Safety/Low Voltage directive) and 89/336/EEC (EMC directive).

Any other region where the regulatory requirements are satisfied by compliance to the standards declared above.

This Declaration of Conformity is issued by **Intel Corporation**, which is solely responsible for the declared compliance.

Place of issue / Declaring company address:

Intel Corporation
5200 NE Elam Young Parkway
Hillsboro, Oregon 97124
USA

Date of Issue: January 22, 2007

Tri D.Than is the manufacturer's representative with the authority of Intel Corporation management to make this Declaration.

Copies of this Declaration of Conformity may be downloaded at: http://developer.intel.com/design/litcentr/ce_docs/index.htm