



# Declaration of Conformity

**Equipment type: Intel® Xeon™ Processor with 800MHz system bus,  
product codes BX80546KG3400EA, BX80546KG3400EP and BX80546KG3400EU**

The equipment described above is declared to be in conformity with the following applicable national and international standards, when tested in a representative chassis. The conformity is valid only when the equipment is used in a manner consistent with the manufacturer's recommendations and the reference documents.

Document no. / Edition / Date of issue	Title
<i>EMC:</i>	
EN 55022:1998, Class B	Information Technology Equipment – Radio disturbance characteristics – Limits and methods of measurement
EN 55024:1998 + Amendment A1:2001	Information Technology Equipment – Immunity Characteristics – Limits and methods of measurement
<i>Safety/Low Voltage:</i>	
EN 60950-1: 2001	Safety of Information Technology Equipment – Part 1: General Requirements

**Additional information:**

EMC test house: Northwest EMC, Inc.  
22975 NW Evergreen Parkway  
Hillsboro, OR 97124  
USA

Safety test house: Underwriters Laboratories, Inc.  
2600 NW Lake Road  
Camas, WA 98607  
USA

**Regions for which conformity is declared:**

European Economic Area (EEA): Intel Corporation declares the equipment in compliance with the essential requirements of EC Council Directives 73/23/EEC (Safety/Low Voltage directive) and 89/336/EEC (EMC directive).

Any other region where the regulatory requirements are satisfied by compliance to the standards declared above.

This Declaration of Conformity is issued by **Intel Corporation**, which is solely responsible for the declared compliance.

**Place of issue / Declaring company address:**

Intel Corporation  
5200 NE Elam Young Parkway  
Hillsboro, Oregon 97124  
USA

Date of Issue: 9 June 2004

Brian Slowik is the manufacturer's representative with the authority of Intel Corporation management to make this Declaration.

Copies of this Declaration of Conformity may be downloaded at: [http://developer.intel.com/design/litcentr/ce\\_docs/index.htm](http://developer.intel.com/design/litcentr/ce_docs/index.htm)