

## **Declaration of Conformity**

## Equipment type: Intel® desktop board, model D910GLDW

The equipment described above is declared to be in conformity with the following applicable national and international standards, when tested in a representative chassis. The conformity is valid only when the equipment is used in a manner consistent with the manufacturer's recommendations and the reference documents.

| Document no. / Edition /<br>Date of issue  | Title  |  |
|--|--|--|
| FCC rules part 15, Class B   | Title 47, Code of Federal Regulations, Part 15   |  |
| ICES-003:2004, Class B   | Interference-Causing Equipment Standard – Digital Apparatus  |  |
| EN 55022:1998, Class B   | Information Technology Equipment - Radio disturbance characteristics - Limits and methods of measuremen  |  |
| CISPR 22:1997, Class B   | Information Technology Equipment – Radio disturbance characteristics – Limits and methods of measuremen  |  |
| VCCI V-3/04.04, Class B  | Information Technology Equipment – Radio disturbance characteristics – Limits and methods of measuremen  |  |
| EN 55024:1998 + A1 + A2  | Information Technology Equipment – Immunity Characteristics – Limits and methods of measurement  |  |
| EN 61000-3-2:2000, Class D   | Electromagnetic Compatibility (EMC) – Part 3: Limits – Section 2 – Limits for Harmonic Current Emissions   |  |
| EN 61000-3-3:1995 + A1   | Electromagnetic Compatibility (EMC) - Part 3: Limits - Sect. 3 - Limitation of Voltage Fluctuation and Flicker   |  |
| EN 60950-1: 2001   | Safety of Information Technology Equipment – Part 1: General Requirements  |  |
| IEC 60950-1: 2001, 1 <sup>st</sup> edition   | Safety of Information Technology Equipment – Part 1: General Requirements  |  |
| UL 60950-1: 2003, 1 <sup>st</sup> edition  | Safety of Information Technology Equipment – Part 1: General Requirements  |  |
| CSA C22.2 No. 60950-1-03   | Safety of Information Technology Equipment – Part 1: General Requirements  |  |
| Additional information:  | EMC test house:<br>Advance Data Technolog<br>47 14 <sup>th</sup> Lin, Chiapau Tsu<br>Linko, Taipei Taiwan  | safety test house:<br>Underwriters Laboratories, Inc.<br>Camas, WA 98607 USA |
| Canada: Intel Corporation<br>European Economic Area<br>EC Council Directives 7<br>Australia / New Zealand:<br>59 001 798 214; ACN:<br>Japan: Voluntary Control | ddress below) is the responsib<br>n (address below) is the declari<br>(EEA): Intel Corporation decla<br>'3/23/EEC (Safety/Low Voltage<br>Intel Australia Pty Ltd, 111 Pac<br>001 798 214.<br>for Interference by Information |  |
| This Declaration of Confo  | rmity is issued by Intel Corp  | <b>coration</b> , which is solely responsible for the declared compliance    |
| Place of issue / Declaring c<br>Intel Corporation<br>5200 NE Elam Young Par<br>Hillsboro, Oregon 97124<br>USA  |  | Signed:  |
| Date of issue: 05 October 2004<br>Revised: 17 February 2005  |  | Brian Slowik is the manufacturer's representative with the author            |

Copies of this Declaration of Conformity may be downloaded at: http://developer.intel.com/design/litcentr/ce\_docs/index.htm