

EECS 523 Homework # 8

Note: For all problems, assume no iteration is necessary for finding ΔL unless otherwise stated. $v_{SAT} = 10^7$ cm/s.

1. The ratio (I_{SUB}/I_D) at hot-carrier induced breakdown usually falls within the range of 0.05 to 0.2. For this problem, assume that it is equal to 0.05 and is device independent. A $1\mu\text{m}$ CMOS technology has the following NMOS device and process parameters: $t_{OX} = 20\text{nm}$, $x_j = 0.3\mu\text{m}$, $\alpha=0$, $V_T = 0.7\text{V}$.
 - (a) Derive an expression for the hot-electron induced breakdown voltage V_{BD} ($V_{BD} = V_{Ds}$) of the NMOS devices as a function of L_{eff} and V_g .
 - (b) Plot $\log(V_{BD})$ as a function of $\log(L_{eff})$ for $L_{eff} = 0.1\mu\text{m}$ to $100\mu\text{m}$ for $V_g = 2\text{V}$.

2. Use the following parameters for an n-channel MOSFET: $t_{OX}=20\text{nm}$, $x_j = 0.3\mu\text{m}$, $\alpha=0$, $V_T = 0.7$ and $L_{eff} = 1\mu\text{m}$. Based on device degradation results, it is recommended that this n-channel MOSFET should not be operated with $E_M > 2 \times 10^5$ V/cm.
 - a) What is the ratio of I_{SUB}/I_D at this value of E_M ? Determine the recommended maximum supply voltage V_{DD} . Assume $V_g = 3\text{V}$ and that this value of V_g produces the maximum I_{SUB} .
 - b) What is the breakdown voltage of the device with $V_g = 3\text{V}$, assuming that the device breaks down at $E_M = 4 \times 10^5$ V/cm.

3.
 - a) Calculate E_M and I_{SUB} in a non-LDD MOSFET described by the following parameters: $t_{OX} = 20\text{nm}$, $x_j = 0.2\mu\text{m}$, $\alpha=0$, $V_{th} = 0.7\text{V}$, $L_{eff} = 0.5\mu\text{m}$ and $W = 0.5\mu\text{m}$ when the source is grounded, the gate voltage $V_g = 2\text{V}$, and the drain voltage $V_d = 4\text{V}$.
 - b) How much are the maximum E_M and I_{SUB} reduced by adding a $0.1\mu\text{m}$ LDD region having an optimum doping profile? Also, what is the improvement in the device lifetime τ_{DC} from the LDD structure? Assume I_d is unchanged with the addition of the LDD region.

4. An NMOS device has the following parameters: $W = 1\mu\text{m}$, $t_{OX} = 2\text{nm}$, $x_j = 0.06\mu\text{m}$, $\alpha=0$, $V_{th} = 0.3\text{V}$, $L_{eff} = 0.08\mu\text{m}$, $v_{SAT} = 10^7$ cm/s, $V_{DD} = 1.2\text{V}$. Find I_d for $V_{ds} = V_{gs} = V_{DD} = 1.2\text{V}$ (iterate to find ΔL in this problem).