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Patents

- » 6,092,184 Parallel processing of pipelined instructions having register dependency
- >> 5,835,748 Method for different set of instructions that causes a processor to perform different data type operations on different register files that logically appear to software as a single aliased register file
- » 5,787,026 Method and apparatus for providing memory access in a processor pipeline
- >> 5,764,943 Data path circuitry for processor having multiple instruction pipelines