

Architecting Cloud Infrastructure for the Future

Jason Waxman

VP and General Manager

Cloud Platforms Group

Data Center and Connected Systems Group

July 22, 2013

Experiences Today and Tomorrow



Voice & Gestures

Personal assistant
Natural Interaction

20X

'12-'17

growth in speech driven mobile network traffic¹

>22X

increase in smartphones with gesture recognition features¹



Video/Media

Content Delivery
Video Search

16X

'12-'17

increase in mobile video traffic²

4X

'13-'17

increase in servers for media / graphics³



Predictive Analytics

Improve healthcare
Reduce car/aircraft parts failure

43% CAGR

'11-'16

for infrastructure supporting Big Data & Analytics⁴

1. ABI Research Aug 2012
2. Cisco Visual Networking Index: Global Mobile Data Traffic Forecast Update, 2012-2017
3. Intel internal analysis and forecast, 2013
4. IDC Storage Solutions, 2013, doc #241515, May 2013.



Sean Brown
Senior Manager, Innovation

An Inflection Point in Speech

Revolutionary shift from task-centric to user-centric

Speech Recognition

Command & Control

Dictation

Search

Natural Language



Intelligent Systems

Understand

Inform

Be Aware

Ubiquitous

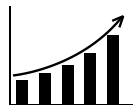
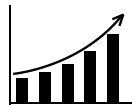
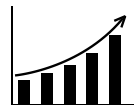
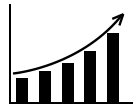
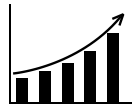
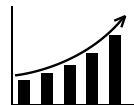
6 billion
mobile
handsets

70+
languages

65+
countries

Delivering an Optimal UX Globally

Only the cloud can deliver the compute power required

Accuracy	Speed	New Features
 Models	 Users	 Data
 Languages	 Transactions	 Platforms

Intel & Nuance Collaboration

The Nuance Cloud, powered by Intel

Nuance Data Centers
Powered by Intel Xeon Processors

Balanced Computing
Device and cloud data centers leveraged for optimal performance

Joint Optimizations on Intel-based Platforms

Hardware + Software
Accelerators

Scale Natural Language
Processing

Delivering Great User Experiences

What's Needed?



Architecting Cloud Infrastructure for the Future

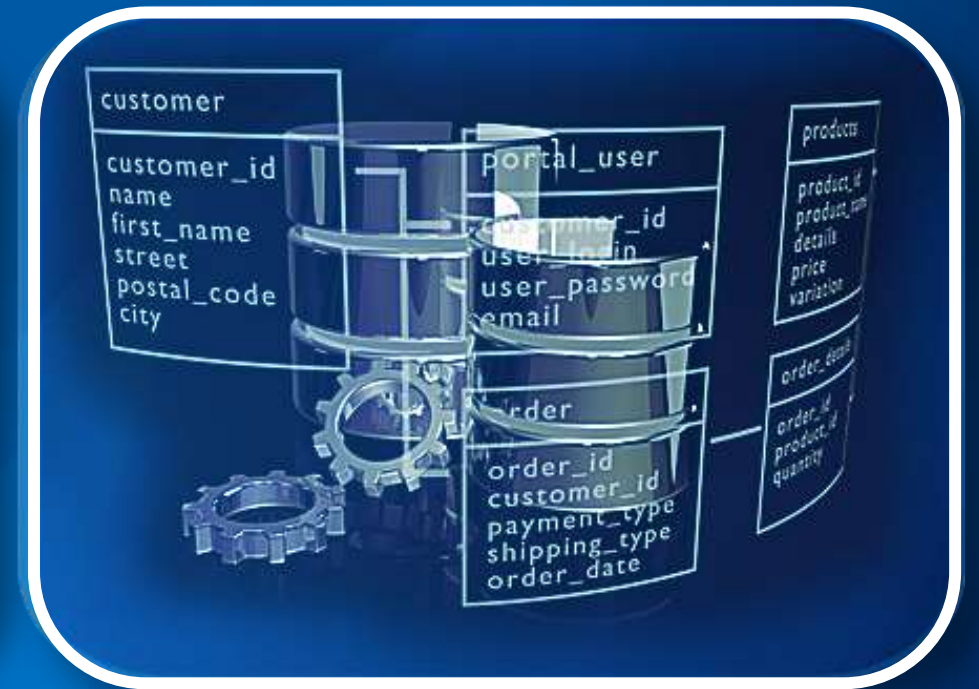
Addressing Requirements



Workload optimized technologies



Composable Resources



Software defined infrastructure

Architecting Cloud Infrastructure for the Future

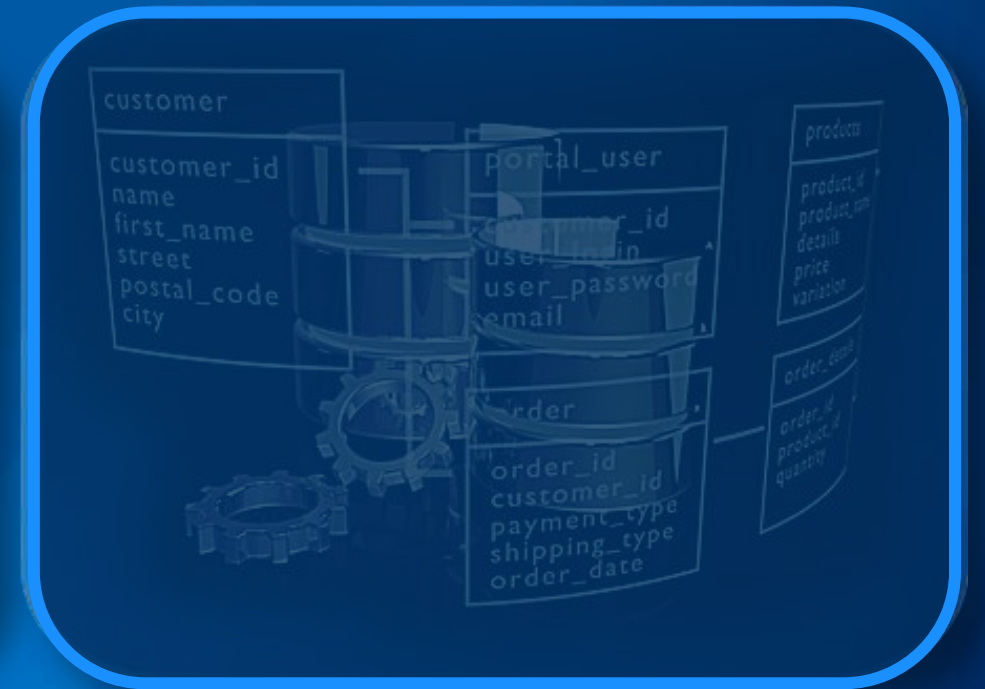
Addressing Requirements



Workload optimized technologies



Composable Resources



Software defined infrastructure

Workload Optimized Technologies

Diversity of Requirements

Servers

CPU & Memory Intensive



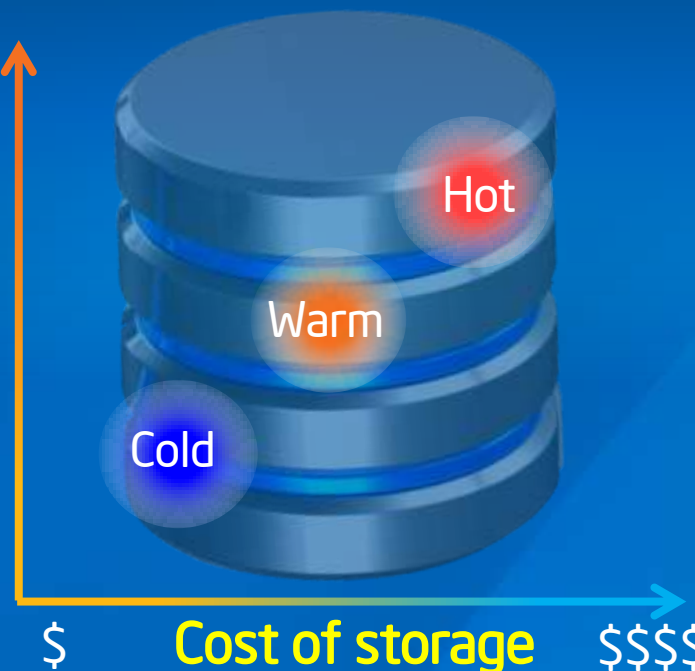
I/O Intensive

Storage

Frequent

Access

Infrequent



CPU Intensive

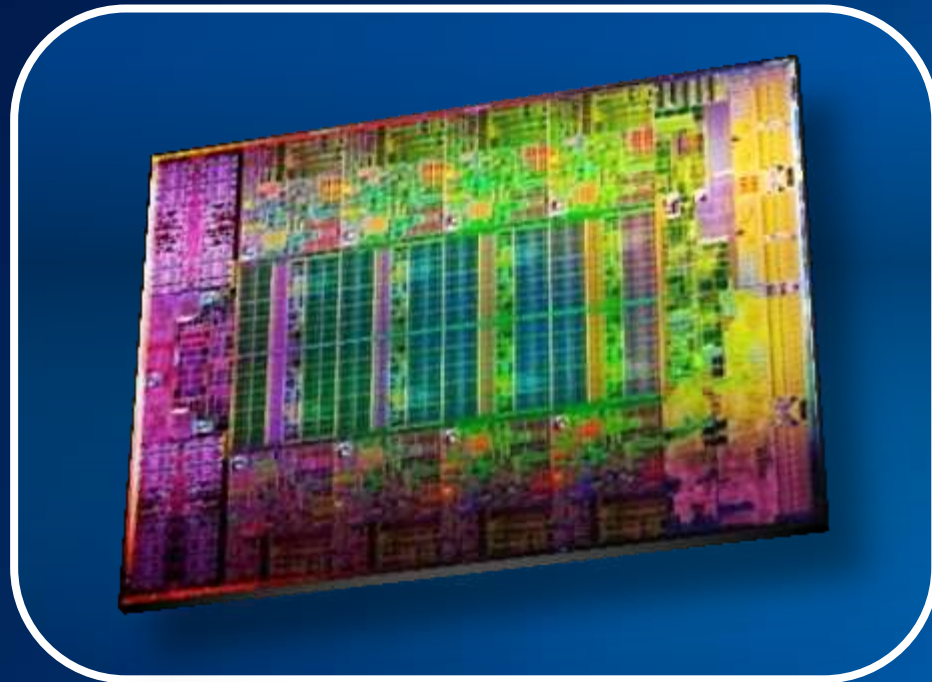


I/O Intensive

Workload Optimized Technologies

Intel Optimizations

CUSTOMIZED SILICON



ACCELERATORS



EXTREME LOW POWER



Intel® Atom™ Processor C2000 Product Family

2nd Generation 64 bit Workload Optimized SoCs

Qual samples shipping



"Avoton" & "Rangeley"

Highly Scalable

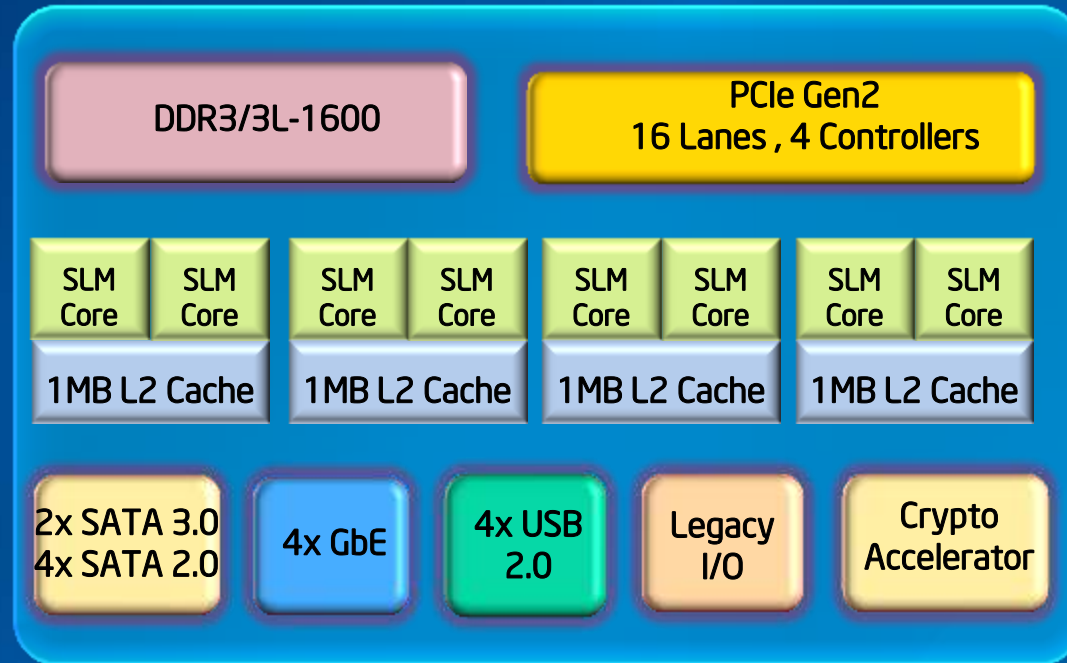
Up to **8** cores with integrated I/O

Higher Performance

Up to **7x** faster^{1,2}

Datacenter Class Features

64-bit, ECC memory, Intel® Virtualization Tech



Higher Efficiency

Up to **4x** higher performance per watt^{1,3}

IA Software Compatibility

Workload Optimized

8x (64GB) Memory capacity¹
Intel® QuickAssist Technology

2.5X increase in system designs for microservers, network and storage

¹vs Intel® Atom™ S2100. Intel Atom C2000 pre-production silicon measurements

SLM= Next Generation Atom® micro architecture "Silvermont"

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.. For more information go to [http://www.intel.com/performance](#). Copyright © 2013, Intel Corporation. Configuration: ²Dynamic Web Benchmark: Atom S1260(8GB,SSD,1GbE), Score=1522. Atom C2xxx(32GB, SSD,10GbE), score=11109. ³Estimated SPECint*_rate_base2006 score for Atom S1260(8GB, HDD) 18.7, est. pwr=20W, Atom C2xxx(16GB, HDD), est. score=69, est. node pwr=19. Intel Internal measurements as of July 2013. Refer to backup for additional details. * Other names and brands may be claimed as the property of others.

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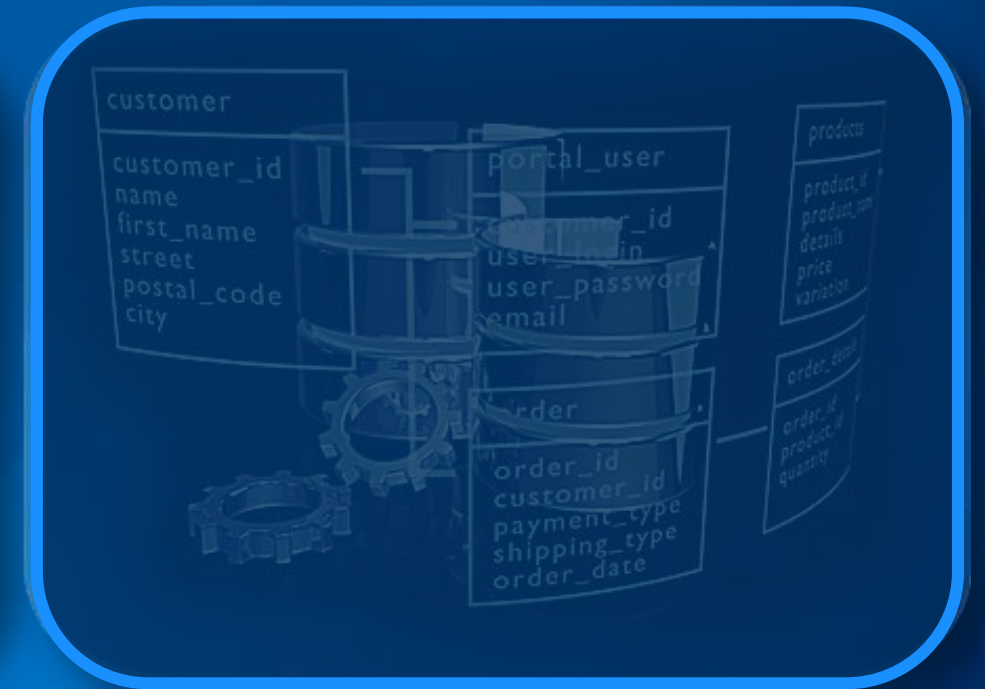
Addressing Requirements



Workload optimized technologies



Composable Resources



Software defined infrastructure

Composable Resources

Evolution of Rack Scale Infrastructure

Today

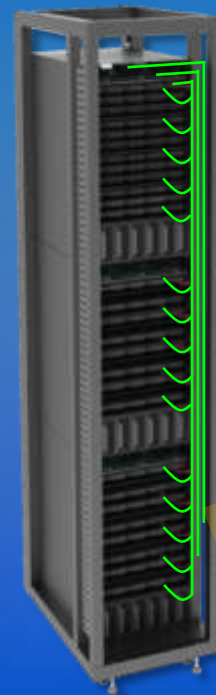
Physical Aggregation



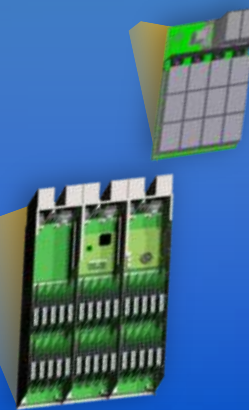
Shared Power
Shared Cooling
Rack Management

Next

Fabric Integration

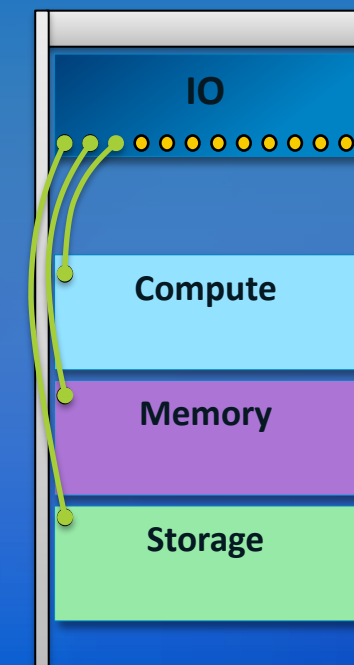


Rack Fabric
Optical Interconnects
Modular refresh



Future

Fully Modular Resources



Pooled compute
Pooled storage
Pooled memory
Shared boot

Enable flexible & efficient datacenters

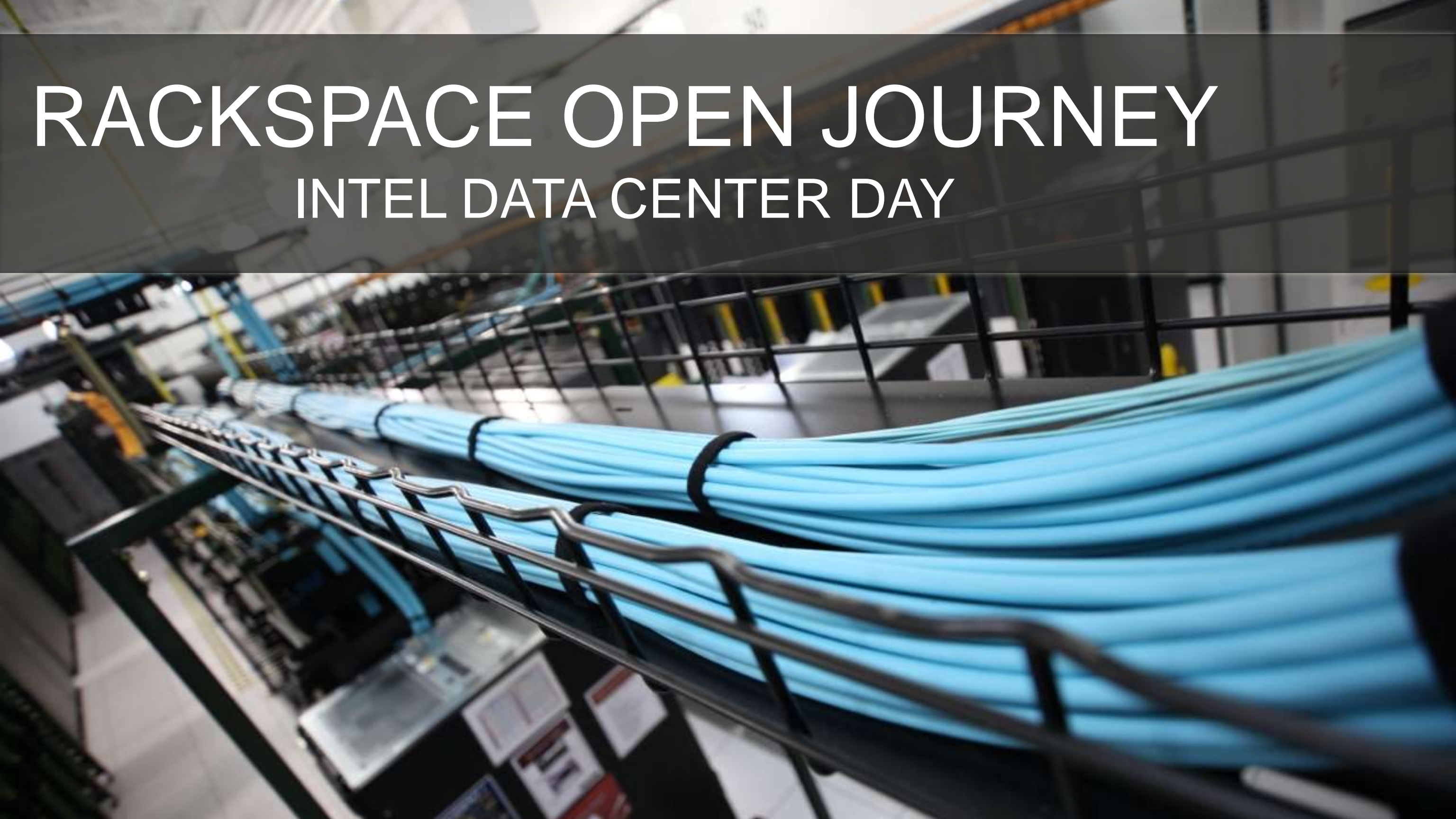




Mark Roenigk
Chief Operating Officer

RACKSPACE OPEN JOURNEY

INTEL DATA CENTER DAY



About Rackspace

205,000+ CUSTOMERS
100,000+ SERVERS
26,000+ VM
≈70 PB STORED

GLOBAL FOOTPRINT
CUSTOMERS IN 120+ COUNTRIES

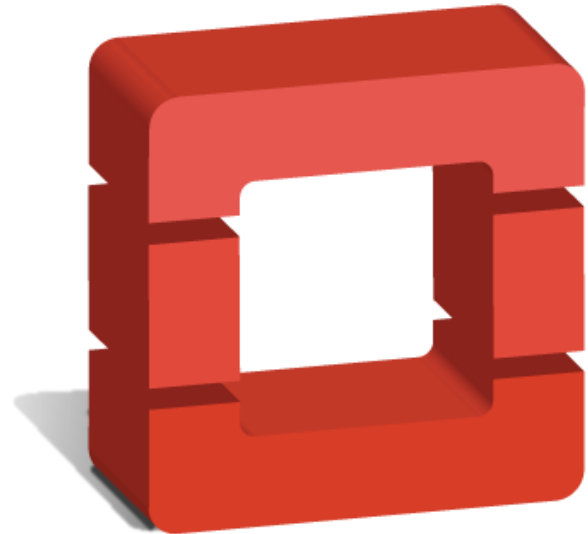


10 WORLDWIDE DATA CENTERS

FORTUNE®
100 BEST COMPANIES TO WORK FOR



We're on the "Open Journey"



openstackTM
CLOUD SOFTWARE



OPEN
Compute Project



The Rackspace OpenRack Server

Using Intel technologies for servers and networking

Rackspace Innovating Faster with Intel® Xeon® family

- Take advantage of greater VM density
- Improved revenue per WATT
- Great partnership
- Accelerated time to market
- A strong product roadmap
- OCP deployment will include Intel 10G implementation

Rackspace OpenRack Design



Quanta server design

- 4 sleds per chassis
 - Still in development
 - Includes hot-swap fans
 - Improved cable routing
- Sled configuration:
- 2x16 core CPUs (16 physical + 16 logical)
 - Up to 256 GiB RAM (16 slots)
 - 2x2.5" SATA hard drive slots
 - 2x10 Gbit Ethernet on mezzanine card, also includes VGA & remote KVM
 - 2x10 Gbit Ethernet on PCI-E slot
 - RAID controller w/non-volatile cache



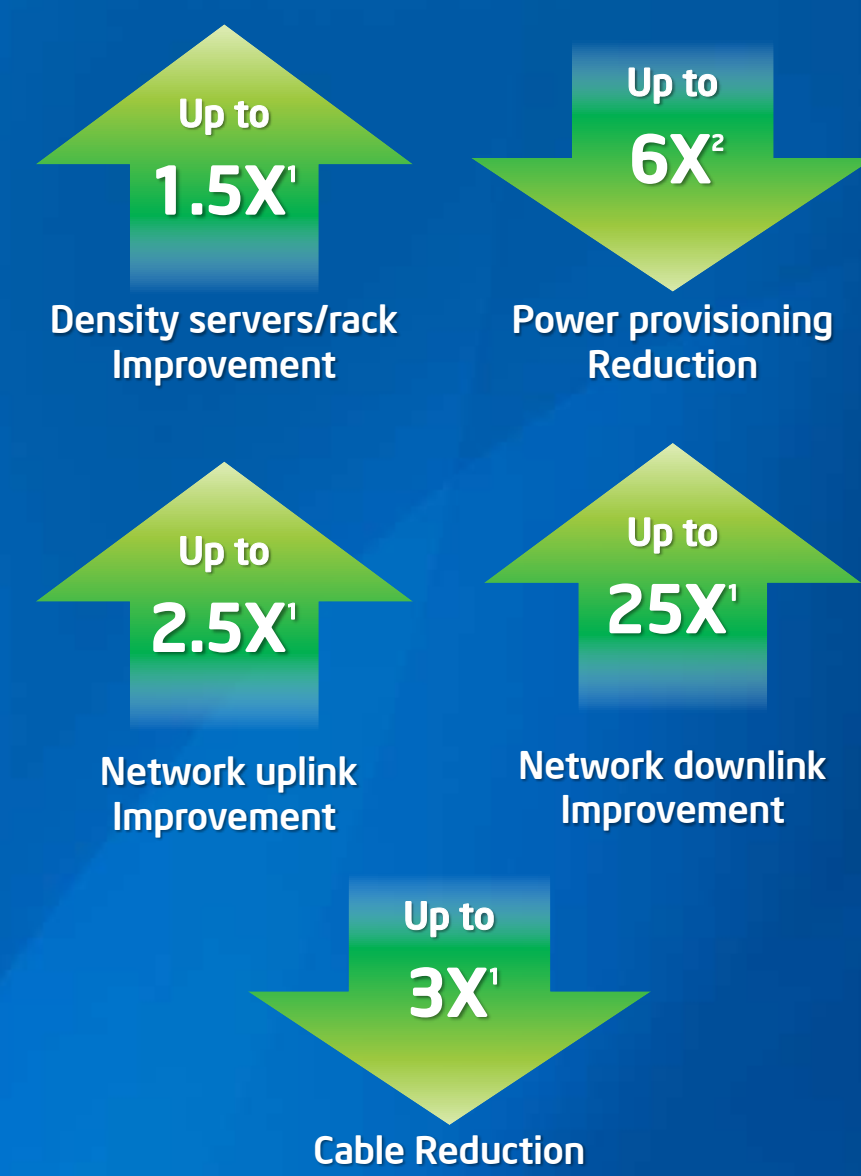
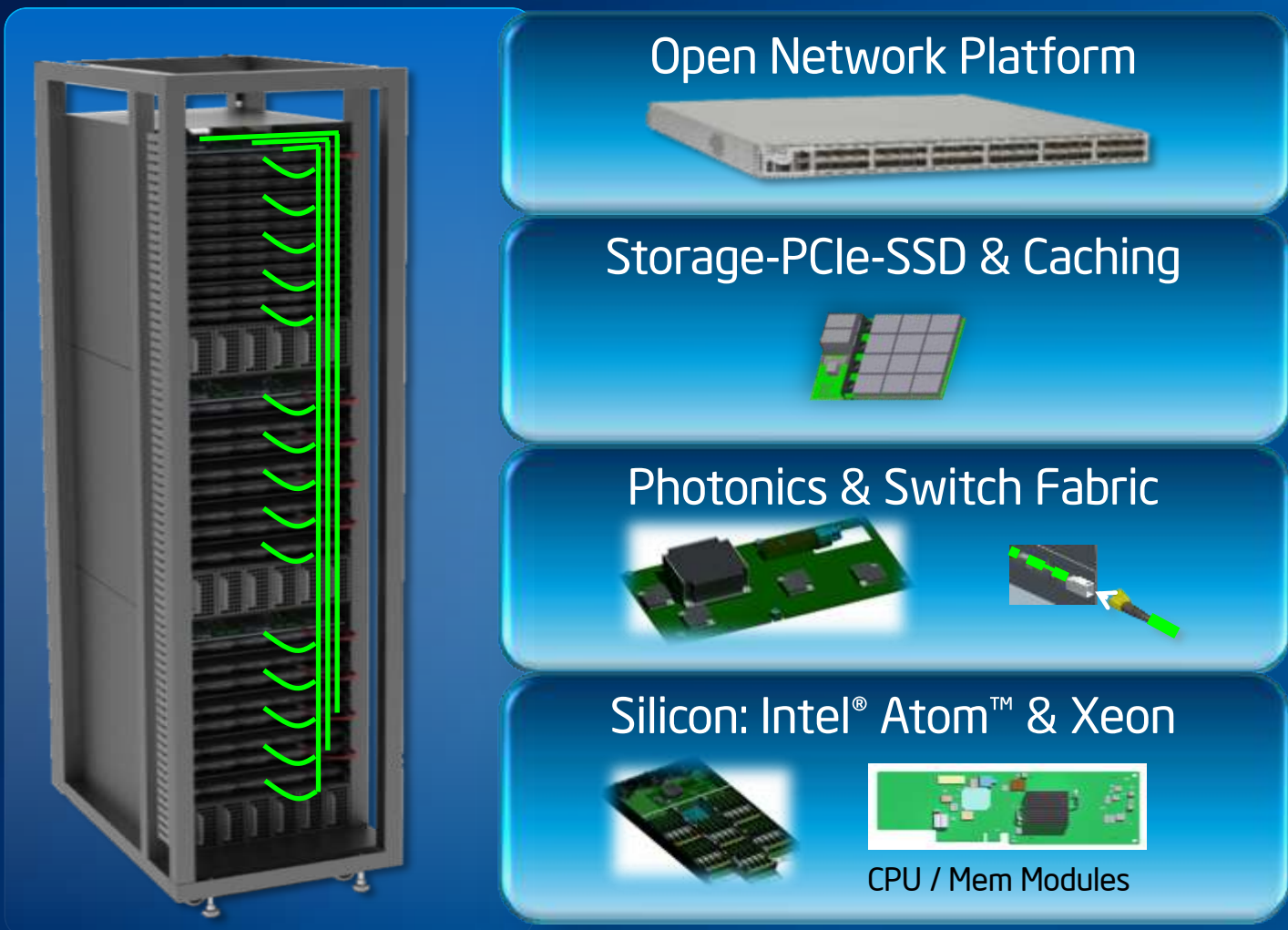
Wiwynn server design

- 3 sleds per chassis
 - Already in production
- Sled configuration:
- 2x16 core CPUs (16 physical + 16 logical)
 - Up to 256 GiB RAM (16 slots)
 - 3x3.5" SATA hard drive slot
 - 2x10 Gbit Ethernet on mezzanine card, also includes VGA & remote KVM
 - 2x10 Gbit Ethernet on PCI-E slot
 - RAID controller w/non-volatile cache



Composable Resources

Intel Rack Scale Architecture Innovation



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1. Improvement based on standard rack with 40 DP servers, 48 port ToR switch, 1GE downlink/server and 4 x10GE uplinks, Cables: 40 downlink and 4 uplink vs. rack with 42 DP servers, SiPh patch panel, 25Gb/s downlink, 100Gb/s uplink, Cables: 14 optical downlink, and 1 optical uplink. Actual improvement will vary depending on configuration and actual implementation.

2. Improvement as compared to 20 Dell PowerEdge R720, N+1 redundant power, 705W PSU x2, peak power provisioned 30,000 Watts vs. same server, shared DC power using 1 power shelf of 7x 700W modules and 4200W (N+1): power provisioned 4900 Watts

http://www.opencompute.org/wp/wp-content/uploads/2013/01/Open_Compute_Project_Power_Shelf_v0.3.pdf, http://www.spec.org/power_ssj2008/results/res2012q4/power_ssj2008-20121030-00569.html ..



Architecting Cloud Infrastructure for the Future

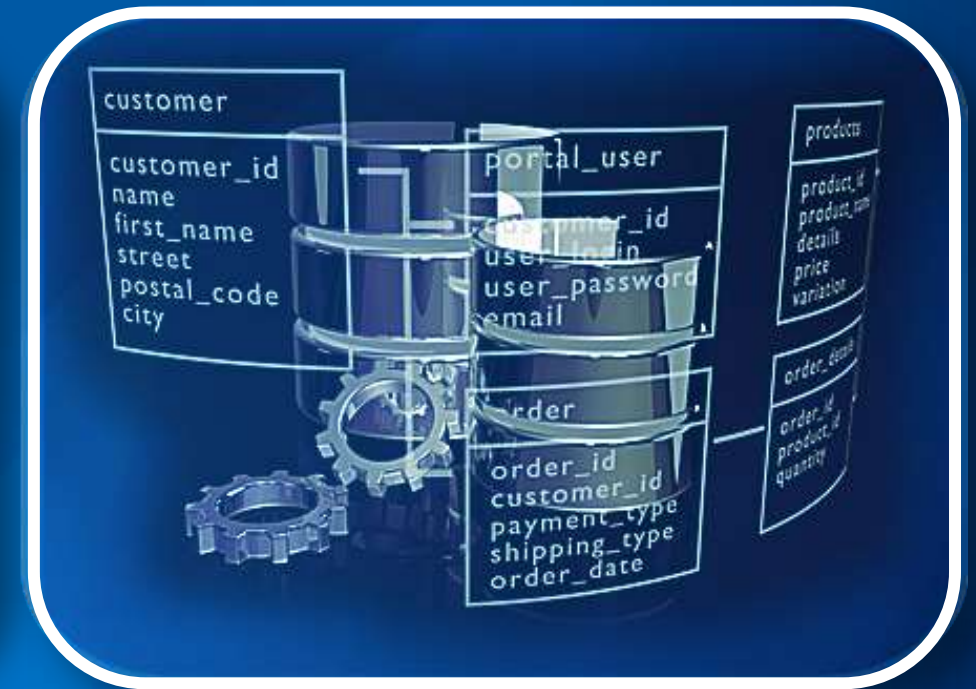
Addressing Requirements



Workload optimized technologies



Composable Resources



Software defined infrastructure

Software Defined Infrastructure

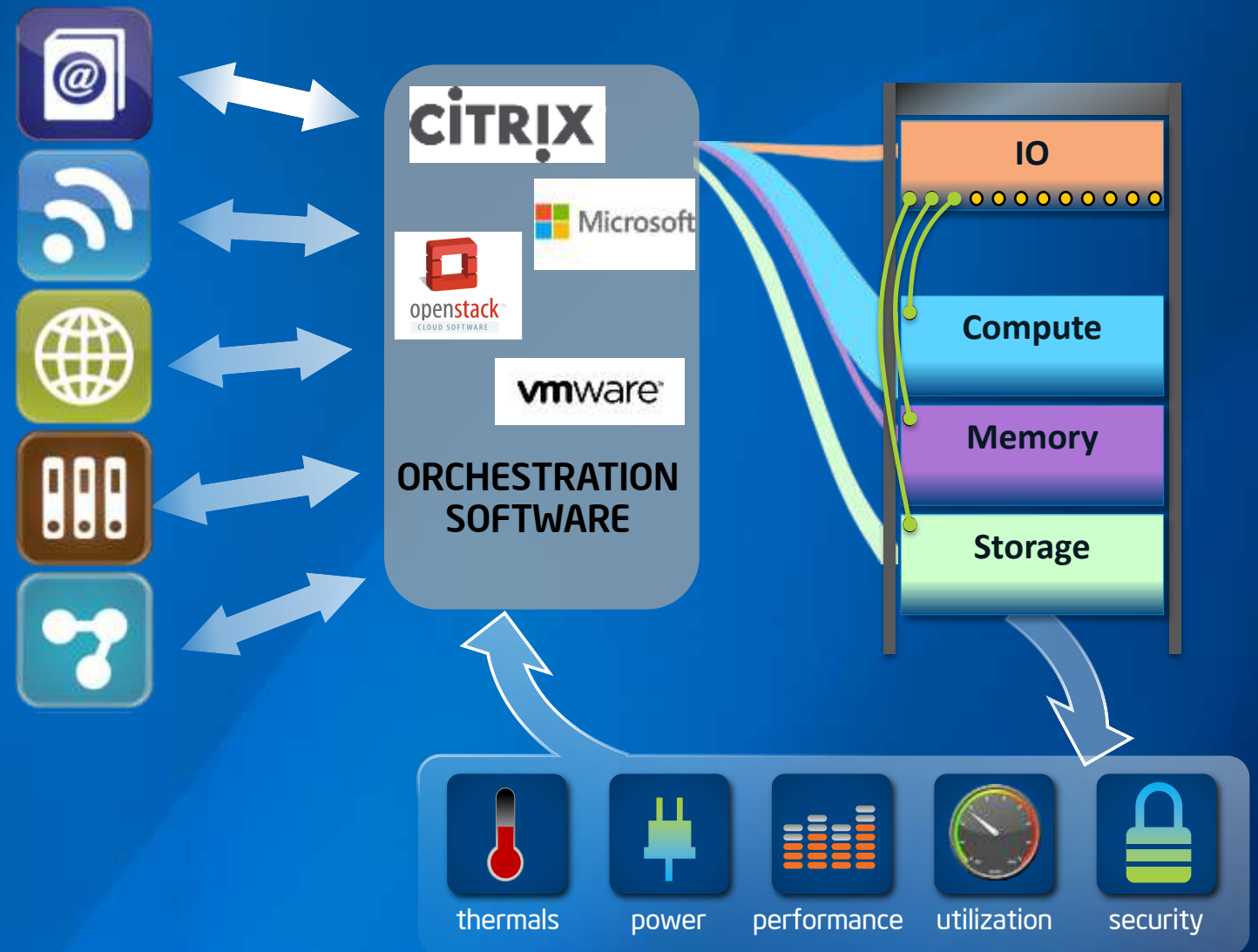
The Need for Better Workload Orchestration

TODAY'S CHALLENGES

- Pressure to meet SLAs
- Overprovisioning of resources
- Inefficient workload placement



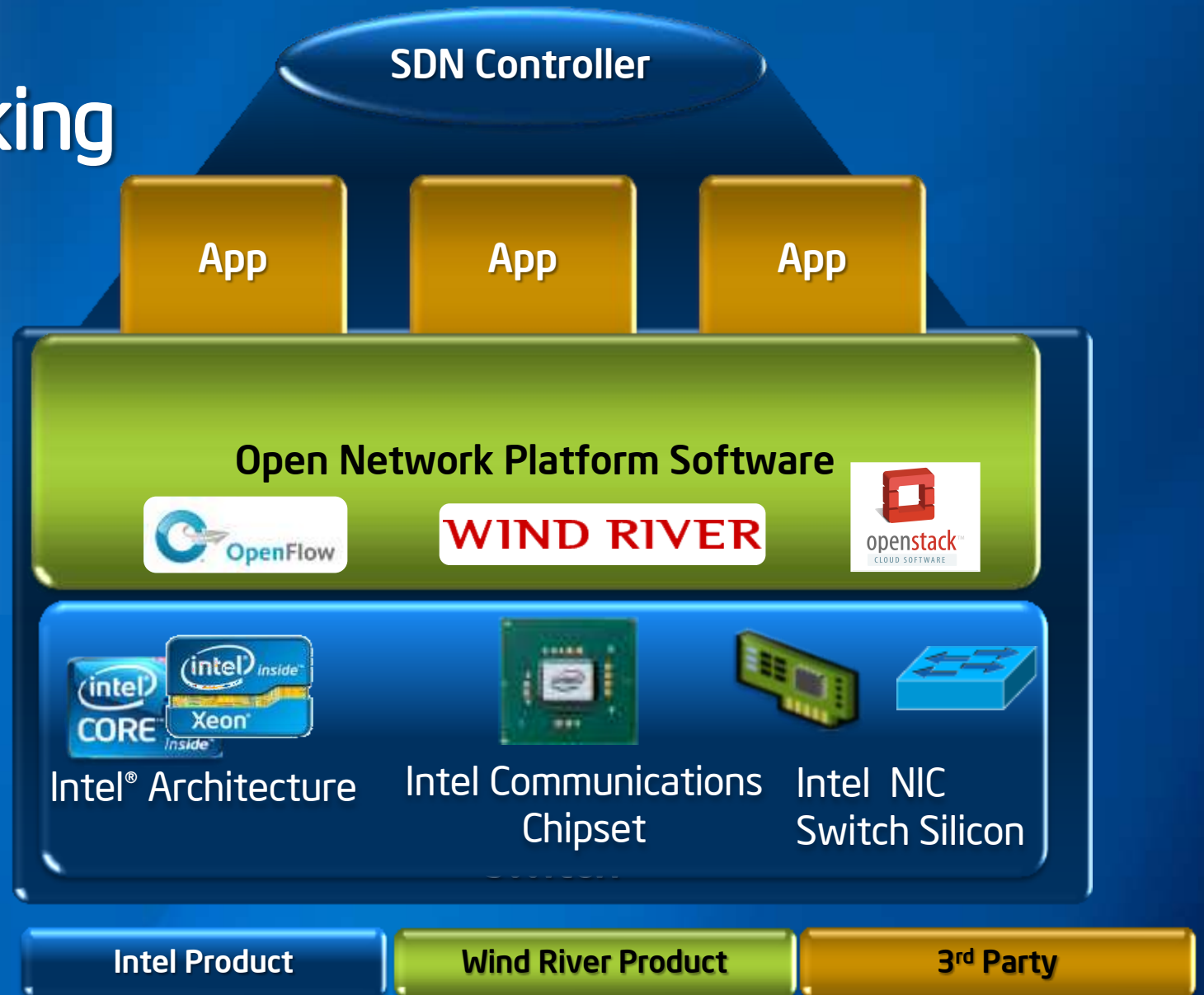
FUTURE: INTELLIGENT ORCHESTRATION



Software Defined Infrastructure

Intel: Accelerating Software Defined Networks

Building blocks for Open Networking
Programmable platform
Enable SDN with Open APIs



Architecting Cloud Infrastructure for the Future

Opportunities in Cloud Systems by 2016

	CAGR	'16 Revenues
Public Cloud Servers	25%	\$15B ¹
Distributed Storage	50%	\$21B ²
SW-Defined Networking	175%	\$5.5B ³

1. Intel forecast, 2013

2. IDC Storage Solutions, 2013, doc #241515, May 2013

3. SDN Market Sizing Plexxi, Lightspeed Venture Partners, SDN Central April 2013; Markets & Markets - 2013



Delivering Great User Experiences



WIND RIVER



Cache
Acceleration
Software



Rack Scale
Architecture



Software Defined
Networking

Server



Intel® CPUs & Chipsets
SOCs & Accelerators

Network

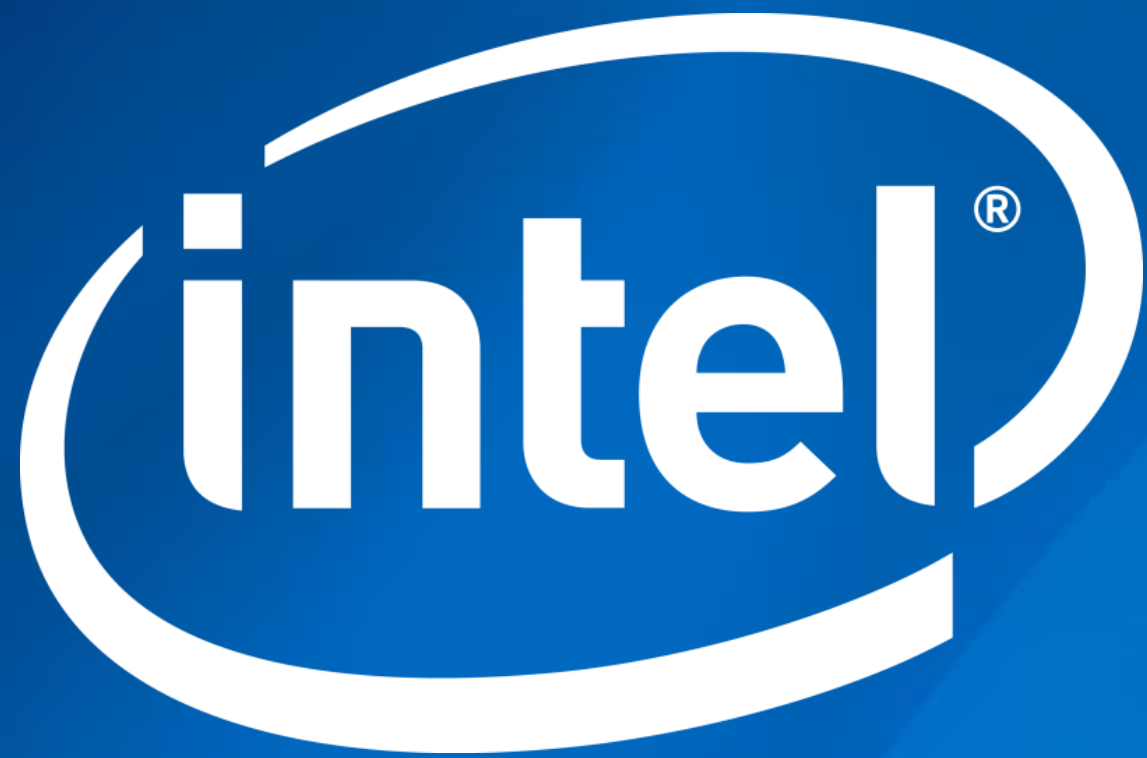


Intel 10GbE
Intel Switch Silicon
Intel Silicon Photonics

Storage



Flexible building blocks
Intel NVM & SSD



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The gross margin percentage could vary significantly from expectations based on capacity utilization; variations in inventory valuation, including variations related to the timing of qualifying products for sale; changes in revenue levels; segment product mix; the timing and execution of the manufacturing ramp and associated costs; start-up costs; excess or obsolete inventory; changes in unit costs; defects or disruptions in the supply of materials or resources; product manufacturing quality/yields; and impairments of long-lived assets, including manufacturing, assembly/test and intangible assets. The tax rate expectation is based on current tax law and current expected income. The tax rate may be affected by the jurisdictions in which profits are determined to be earned and taxed; changes in the estimates of credits, benefits and deductions; the resolution of issues arising from tax audits with various tax authorities, including payment of interest and penalties; and the ability to realize deferred tax assets. Gains or losses from equity securities and interest and other could vary from expectations depending on gains or losses on the sale, exchange, change in the fair value or impairments of debt and equity investments; interest rates; cash balances; and changes in fair value of derivative instruments. Intel’s results could be affected by adverse economic, social, political and physical/infrastructure conditions in countries where Intel, its customers or its suppliers operate, including military conflict and other security risks, natural disasters, infrastructure disruptions, health concerns and fluctuations in currency exchange rates. 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Intel® Atom™: Performance Configuration Details

Dynamic Web Performance:

Atom S1260: DBC SDP w/Intel® Atom™ S1260 (2.0GHz, 2C), Hyper-Threading Enabled, 1x8GB DDR3-1333 MHz UDIMM ECC, BIOS version D134.4, Fedora* 17, Linux Kernel 3.3.4-5fc.x86_64, Apache 2.2.22, PHP 5.4.7, Boot Drive 1x 150GB SSD, Addl Drive 2x 150GB SSD, 2xGbE, Score: 1522

Atom C2xxx: MPK SDP w/Intel® Atom™ C2xxx (8C), Turbo Disabled, 4x8GB DDR3-1600 MHz UDIMM ECC, BIOS version 18D05, Fedora* 17, Linux Kernel 3.3.4-5fc.x86_64, Apache 2.2.22, PHP 5.4.7, Boot Drive 1x150GB SSD, Addl Drive 1x 800GB SSD, 1x10GbE, Score: 11109



Performance Per Watt:

Atom S1260: FOR.INTEL.cpu2006.1.2.ic13.1.linux64.01june2013
Supermicro* 5017A-EF with one Intel® S1260 processor (2-core 2.0GHz), EIST Enabled, Hyper-Threading Enabled, 8GB memory (1x 8GB DDR3-1333 UDIMM ECC), 250GB SATA 7200RPM HDD, Red Hat Enterprise Linux 6.4 . Estimated score:int_rate_base2006=18.7. Est. Power=20w

Atom C2xxx: FOR.INTEL.cpu2006.1.2.ic13.1.linux64.01june2013
Intel® Mohon Peak Alpha platform with one Intel® Avoton processor (8-core), Turbo Boost Disabled, 16GB memory (2x 8GB DDR3-1600 UDIMM ECC), 250GB SATA 7200RPM HDD, Red Hat Enterprise Linux 6.4. Estimated score:int_rate_base2006=69, Est. Power=19w



Estimated Xeon® E3-1285L v3 vs. nVidia* GTX 680 Comparison

		nVidia* GTX 680	Xeon E3-1285L v3	Xeon E3 Advantage
				
Per Node	No. of Transcodes	8	10	up to 25% more
	Power (1 CPU + 1 GPU)	290W	65W	
Per Rack	No. of Transcodes	336 (42 GPUs)	1,560 (156 GPUs)	up to 4.6x better
	Power	14,524W	10,865W	
	3yr TCO per Transcode	\$353.52	\$127.42	up to 64% lower

Platform configurations: Discrete graphics: 1U server with two Intel® Xeon® processor E5-2650, 2.0 GHz, 20MB cache, 8GT/s QPI, 8x1GB DDR3-1333 memory, 1 hard drive, 1 power supply, 2 nVidia* GTX680 graphics cards; Xeon E3v3: 3U microserver with twelve Intel® Xeon® processor E3-1285Lv3, 3.1 GHz, 8MB cache, 4x1GB DDR3-1333, 1 hard drive, 1 power supply. Concurrent transcode per GPU: 8 (Discrete graphics), 10 (E3v3). GTX680 transcode performance from nVidia* whitepaper at http://international.download.nVidia.com/webassets/en_US/pdf/GeForce680Whitepaper.pdf. Intel E3 transcode performance came from Intel whitepaper: Intel Quick Sync Video Technology on Intel® Iris™/Iris Pro Graphics (5100+ Series and Intel HD Graphics (4200+ Series)—Flexible Transcode Performance and Quality Explained.

* Results, which are provided for informational purposes only, were estimated as of March 2013 by Intel based on a comparison of performance (number of 1080p 30fps HD concurrent transcode per GPU) and related results utilizing Intel's product data and data published by nVidia* Kepler*. Any difference in system hardware or software design or configuration may affect actual performance. Intel does not control or audit the design or implementation of third party data referenced in this document. Intel encourages all of its customers to visit the websites of the referenced third parties or other sources to confirm whether the referenced data is accurate and reflects performance of systems available for purchase.

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