LET'S SEE WHAT DEVELOPS. IDF2013 INTEL DEVELOPER FORUM



Sponsors of Tomorrow.





Transforming Experiences From Devices to Datacenters

Diane Bryant

Senior Vice President & General Manager Datacenter and Connected Systems Group



Sponsors of Tomorrow.



Exceptional Experiences















Delivered by Amazing Devices



Reaching Beyond Consumers to IoT



All Powered by the Datacenter

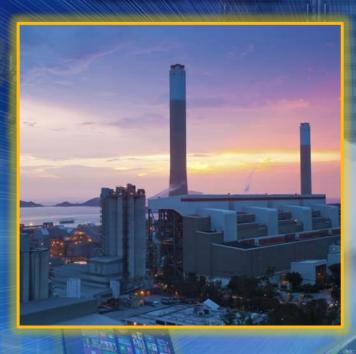


Helping Build Smarter Cities, Healthier Communities and Thriving Businesses

Global Supply Chains



Energy Conservation



Urban Growth



Medical Costs

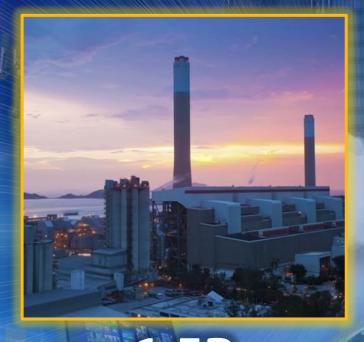


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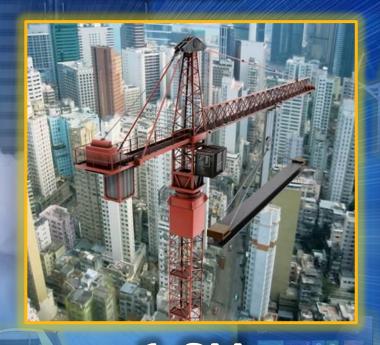


4B RFID Tags* Energy Conservation



1.5B
Electrical
Utility Meters*

Urban Growth



1.6M
PRC Traffic
Cameras*

Medical Costs



3BHuman DNA
Base Pairs*

Enabling Exceptional Experiences







Edge to
Datacenter
Analytics

Application
Optimized
Datacenter

Breakthrough Innovation

Enabling Exceptional Experiences







Edge to
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Application
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Breakthrough Innovation

The Demands Of "BIG DATA"

Shrinking response time: days -> hours -> seconds

1100 00

Expanding data types: users, sensors, machine to machine

Growing data sets: GB-> TB-> PB







Device to Datacenter Solution

Improves Safety, Reduces Congestion



Edge Intelligence



Datacenter **Analytics**



Intel® Xeon® E3 Intel® Xeon® E5 For Video Analytics & Filtering

For System Wide Analytics

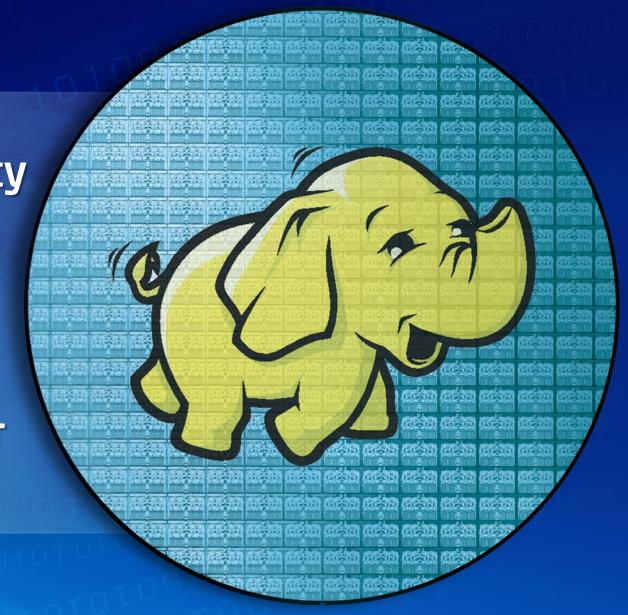
Investment in Hadoop

Contributing code to open source community

Optimized distribution for Xeon®

Hardware-enhanced compression

Automated tuning with Intel® Active Tuner



Analytics Example: China Mobile Group

Challenge: Deliver real time call data records

Real-time retrieval of 30 days records

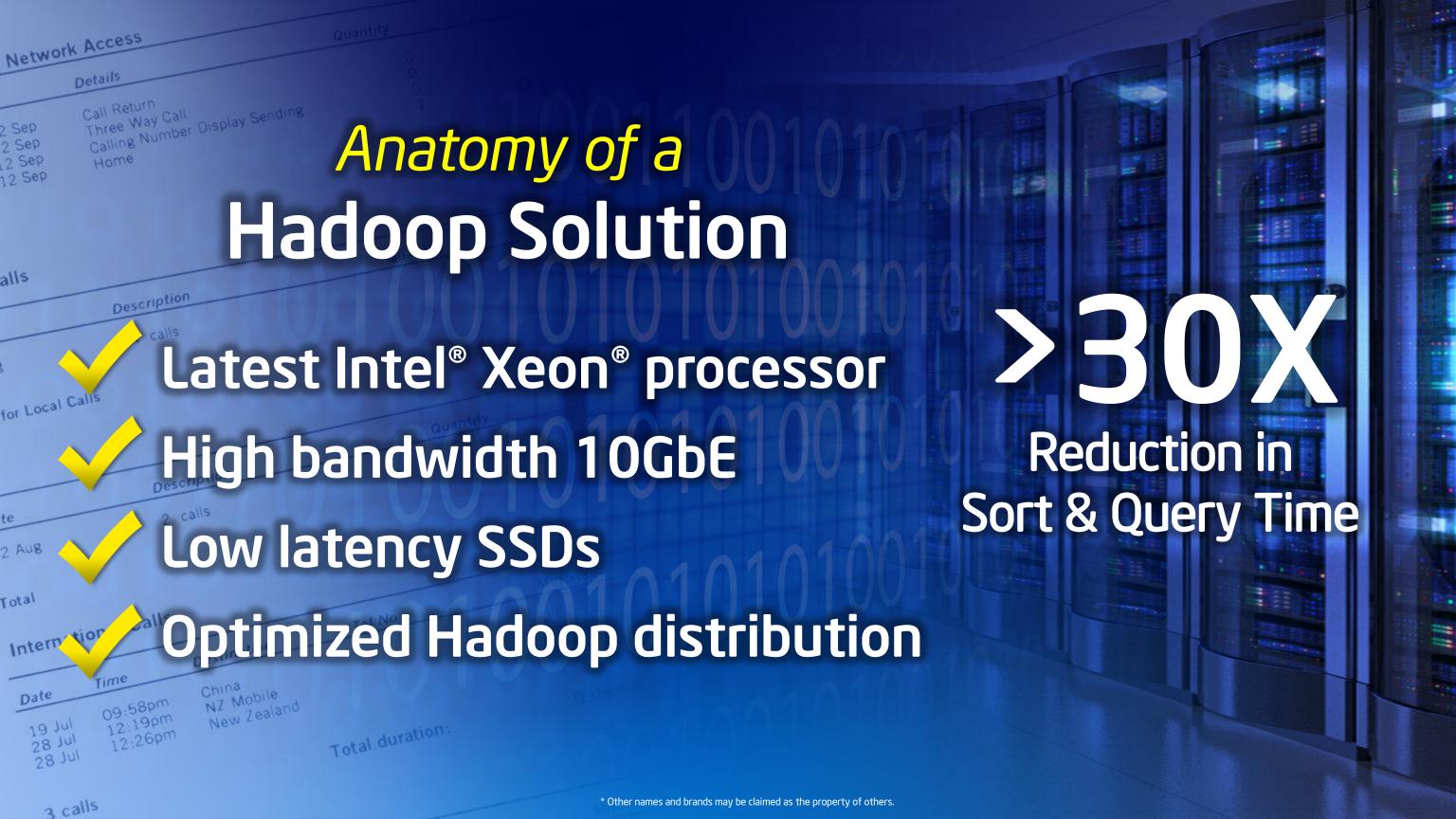
300K records generated every second

35MB per second; 90TB billing data/month









Analytics Powering Zhejiang's Smart Grid

Challenge: Improve grid efficiency

In-memory database supporting 100TB and 1M concurrent requests

Reduced waste and carbon emissions

Sensors supported:

100M+

Accelerate decisions:

60x



Enabling Exceptional Experiences







Edge to
Datacenter
Analytics

Application
Optimized
Datacenter

Breakthrough Innovation

Diversity of Application Requirements

Global Supply Chains



Transaction Processing

Energy Conservation



In-Memory Analytics

Urban Growth



Graphics Processing

Medical Costs



I/O Bandwidth

Complete Range of Datacenter Solutions

Server



Storage



Network











The World's Broadest Datacenter IP Portfolio

Networking and I/O

Intel® QuickPath Interconnect

Intel®True Scale infiniband

Intel® Ethernet Switch Controller

Intel® I/O Acceleration Technology

Wind River* Open Network Software

Intel® Data Direct IO

Intel® Integrated I/O

Intel® Ethernet 10Gb

Storage and Memory

Intel® Cache Acceleration Software

Intel® SSD

Intel® Direct Memory Access

Intel® Rapid Storage Technology

NVM Express

Non-transparent bridging

Lustre*

Asynchronous DRAM Refresh

Compute

Intel® Hyper-Threading Technology

Intel® AMT

Intel® Datacenter Manager

Intel® Turbo Boost Technology

Intel® Node Manager

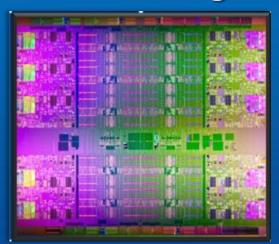
Intel® Virtualization Technology

Intel® Run Sure Technology

Intel® AVX

Application Performance at the Speed of Silicon

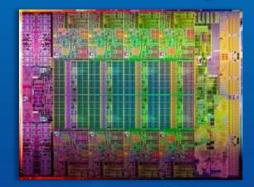
Intel® Xeon® Processor **E7 Family**



4-8S+: Scalability, reliability, memory capacity

Expanding the Dynamic Range of Capabilities in 2013

Intel® Xeon® Processor E5 Family



2S: High performance, power efficient

Intel® Xeon® Processor E3 Family



1S: Economical, general purpose

Intel® Atom™ Processor 1200 Family



1S: Light workloads, lowest power

Upcoming: Next Gen Intel® Xeon® Processor E7 Family "Ivy Bridge-EX"



3X Memory Capacity vs Prior Gen: *Up to* 12TB *in 8S node*

Intel® Run Sure Technology:
Building on Xeon's world-class uptime

All products, computer systems, dates and figures specified are preliminary based on current expectations, and are subject to change without notice.

Upcoming: Next Gen Intel® Xeon® Processor E7 Family "Ivy Bridge-EX"





"SAP is excited by the Intel Xeon processor E7 family's expanded memory capacity for high-performance in-memory database computing."

Franz Färber,

SVP Technology, Innovation and Platforms HANA

All products, computer systems, dates and figures specified are preliminary based on current expectations, and are subject to change without notice.

Upcoming: Next Gen Intel® Xeon® Processor E5 Family "Ivybridge-EP"



22nm process technology
Best-in-class performance
Exceptional energy efficiency
Robust hardware enhanced security

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products.

Upcoming: Next Gen Intel® Xeon® Processor E3 Family "Haswell"





As low as 13W TDP

Integrated graphics for video workloads: 25% more concurrent HD transcodes¹

Media software developer kits: Available for Linux and Windows² in 2013



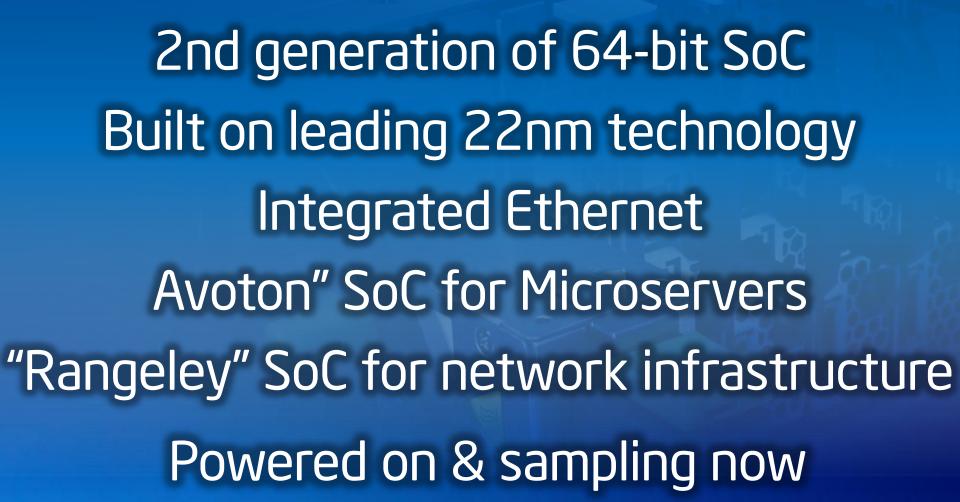
Introducing the Intel® Atom™ Processor S12x9 Product Family for Storage

Up to 40 lanes of integrated PCle* 2.0

Integrated storage intelligence and RAID acceleration

Asynchronous DRAM Refresh and Non-Transparent Bridging

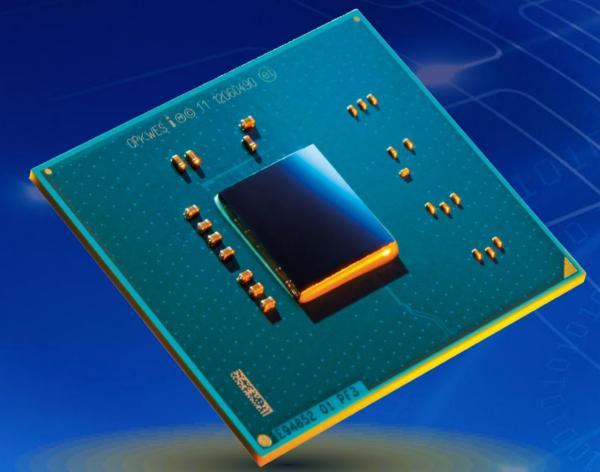


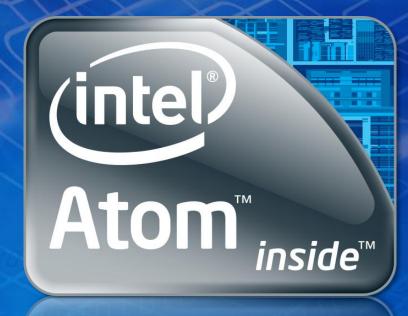






First Ever Live Demo of Avoton





inside

Enabling Exceptional Experiences





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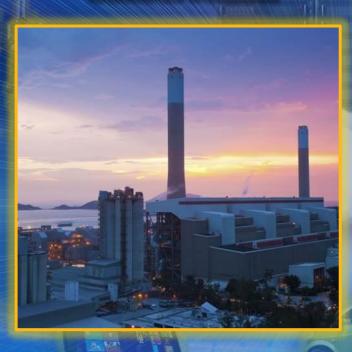
Breakthrough Innovation

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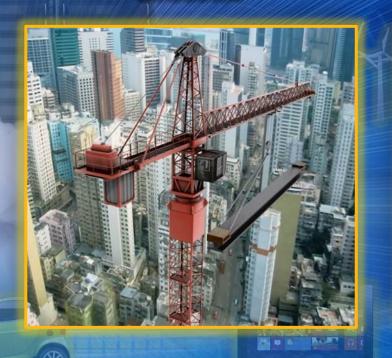
Global Supply Chains



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Urban Growth



Medical Costs



Requires Collaboration And Innovation Across The Industry

Reinventing the Datacenter Rack

Today

Physical Aggregation



Shared Power
Shared Cooling
Shared
Management

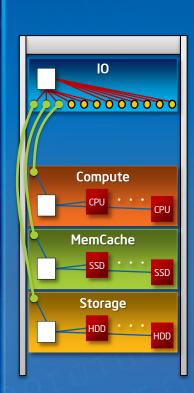
Emerging

Fabric Integration



Future

Subsystem Aggregation



Pooled Compute
Pooled Storage
Pooled Memory
Shared Boot



Eric Chen Chairman, Project Scorpio

Project Scorpio Update

Committee of Project Scorpio



Technical support by: (intel)









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