



Intel[®] Desktop Board D945PVS Specification Update

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The Intel[®] Desktop Board D945PVS may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D945PVS may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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CONTENTS

REVISION HISTORY	v
PREFACE.....	vi
Specification Update for the Intel® Desktop Board D945PVS	1
GENERAL INFORMATION	3
SPECIFICATION CHANGES	5
ERRATA	6



REVISION HISTORY

Date of Revision	Version	Description
May 26, 2005	-001	This document is the first Specification Update for the Intel® Desktop Board D945PVS.
October 2005	-002	Added Erratum 1 and updated the General Information table.
June 2006	-003	Updated Erratum 1. Added Erratum 2. Updated Basic Desktop Board D945PVS Identification Information table.
July 2006	-004	Added Specification Changes 1, Erratum 3 and updated General Information table.
November 2006	-005	Added Specification Changes 2.
February 2007	-006	Added Specification Changes 3.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D945PVS Technical Product Specification* (Order Number D14072). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- *Intel® Pentium® D Processor 900Δ Sequence and Intel® Pentium® Processor Extreme Edition 955Δ, 965Δ Specification Update* (Order Number 310307)
- *Intel® Pentium® D Processor 800Δ Sequence and Intel® Pentium® Processor Extreme Edition 840Δ Specification Update* (Order Number 306832)
- *Intel® Celeron® D Processors 300Δ Sequence Specification Update* (Order Number 302354)
- *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 945G/945GZ/945P/945PL Express Chipset Family Specification Update* (Order Number 307503) for specification updates concerning the 82945P MCH Controller and that may apply to the desktop board D945PVS. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® I/O Controller Hub 7 (ICH7) Family Specification Update* (Order Number 307014) for specification updates concerning the 82801GB ICH7 I/O Controller Hub and that may apply to the desktop board D945PVS. Unless otherwise noted in this document, it should be assumed that any ICH7 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D945PVS's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the
Intel[®] Desktop Board D945PVS**



GENERAL INFORMATION

Basic Desktop Board D945PVS Identification Information

AA Revision	BIOS Revision	Notes
C98862-203	SN94510J.86A.0021	1,2
C98862-204	SN94510J.86A.0037	1,2
C98862-205	SN94510J.86A.0044	1,2
C98862-206	SN94510J.86A.0050	1,2
C98862-207	SN94510J.86A.0057	1,2
C98862-208	SN94510J.86A.0057	1,2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82945P Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82945P MCH	A2	SL8HT
82801GB ICH7	A1	SL8FX
82801GR ICH7R	A1	SL8FY



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D945PVS. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

- Doc: Document change or update that will be implemented.
- PlanFix: This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
- Fixed: This erratum has been previously fixed.
- NoFix: There are no plans to fix this erratum.
- Shaded: This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Change to Section 2.8.2.3, Add-in Card Connectors
2	Doc	Changes to Section 1.9.2 Audio Connectors
3	Doc	Changes to Section 1.10.1 Intel® 82573V/82574V Gigabit Ethernet Controller
NO.	PLANS	ERRATA
1	Plan Fix	Hardware Monitoring and Fan Control Could Fail and Only Run at a Maximum or Minimum PWM for CPU Fan
2	Plan Fix	Digital Noise May Affect Audio Quality When Using the Rear MIC Input on boards with a 6-Channel (5.1) Audio Subsystem
3	Plan Fix	The placement of capacitors behind PCI Express* x1 connector slot 1 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard



SPECIFICATION CHANGES

The Specification Changes listed in this section apply to the *Desktop Board D945PVS Technical Product Specification* (Order Number D14072). All Specification Changes will be incorporated into a future version of that specification.

1. ***The following note will be added to the Technical Product Specification in Section 2.8.2.3 Add-in Card Connectors:***

NOTE

Capacitors required for the audio circuitry are located within the ATX keep-out area near PCI Express x1 slot 1, and may limit the use of some PCI Express x1 cards.*

2. ***The following note will be added to the Technical Product Specification in Section 1.9.2 Audio Connectors:***

INTEGRATOR'S NOTES

Electrostatic discharge (ESD) can damage desktop board components. Front-panel connectors should provide sufficient protection to prevent ESD damage to components inside the chassis enclosure.

3. ***Section 1.10.1 of the Technical Specification will be updated in its entirety as follows to remove reference to Jumbo frame support.***

1.10.1 INTEL® 82573V/82574V GIGABIT ETHERNET CONTROLLER

The Intel® 82573V/82574V Gigabit Ethernet Controller supports the following features:

- PCI Express link
- 10/100/1000 IEEE 802.3 compliant
- Compliant to IEEE 802.3x flow control support
- TCP, IP, UDP checksum offload
- Transmit TCP segmentation
- Advanced packet filtering
- Full device driver compatibility
- PCI Express Power Management Support



ERRATA

1. ***Hardware Monitoring and Fan Control Could Fail and Only Run at a Maximum or Minimum PWM for CPU Fan***

PROBLEM: The Hardware Monitoring and Fan Control may only support up to 3.6V on its Pulse Width Modulation (PWM) outputs. The 4-pin fans used with the Intel Desktop board could have a pull up voltage as high as 5.25V, potentially causing failures in the circuit used for fan control on this Intel Desktop Board.

IMPLICATION: If Hardware Monitoring and Fan Control fails due to this erratum, the CPU fan could run at either 100% PWM, or at a minimum PWM (normally set to 30% PWM for four-pin CPU fans). A failure at 100% PWM could result in louder system acoustics. A failure at minimum PWM may result in the reduced heat sink efficiency being insufficient to fully support the processor thermal requirements at some higher work loads. A variety of conditions such as room temperature, heat sink characteristics, application, workload and chassis design will affect the impact of the PWM failure. If the thermal requirements of the processor are not met, the processor will attempt to reduce its own temperature. Depending on the severity of the ASIC failure, this may result in a noticeable performance reduction.

WORKAROUND: None

STATUS: This erratum has been fixed. Refer to the Product Change Notification 105496-02 available at <http://developer.intel.com/design/pcn/MTHRBRD/index.htm>.

2. ***Digital Noise May Affect Audio Quality When Using the Rear MIC Input on boards with a 6-Channel (5.1) Audio Subsystem***

PROBLEM: Digital noise may affect audio quality when using the rear MIC input on boards with a 6-Channel (5.1) Audio Subsystem. The digital noise can affect the MIC signal via the Codec's MIC bias voltage.

IMPLICATION: Digital noise may be heard on the rear MIC input when systems are used in the following configuration: input monitor enabled in drivers, MIC boost set to 20dB gain, and MIC volume set to maximum. The digital noise may be heard when recording voice, during voice chat, when dragging window around the screen, or when opening and closing windows. No noise issues have been associated with the front panel MIC input.

WORKAROUND: None

STATUS: This erratum may be fixed in a future board revision.

3. ***The placement of capacitors behind PCI Express* x1 connector slot 1 may prohibit some PCI Express x1 add-in cards from properly connecting to the motherboard***

PROBLEM: Due to the design and placement of the audio circuitry on this motherboard, it was necessary to position capacitors in the keep-out zone behind PCI Express* x1 connector slot 1.



IMPLICATION: Some PCI Express x1 add-in cards may not fully connect to PCI Express x1 connector slot 1, due to interference between capacitors in the keep-out zone and I/O connectors on the add-in card that are located on the I/O bracket, in line with the edge fingers on the card.

WORKAROUND: None

STATUS: MAY BE FIXED IN A LATER HARDWARE REVISION.