



Intel® Desktop Board D975XBX Specification Update

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The Intel® Desktop Board D975XBX may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D975XBX may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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REVISION HISTORY

Date of Revision	Version	Description
January 10, 2006	-001	This document is the first Specification Update for the Intel® Desktop Board D975XBX.
July 2006	-002	Updated General Information table.
November 2006	-003	Updated General Information table and added Specification Change 1.



PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D975XBX Technical Product Specification* (Order number D36110). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D975XBX. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 975X Express Chipset Specification Update* for specification updates concerning the 82955X MCH Controller and that may apply to the desktop board D975XBX. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 7 (ICH7) Family Specification Update* for specification updates concerning the 82801GB I/O Controller Hub and that may apply to the desktop board D975XBX. Unless otherwise noted in this document, it should be assumed that any ICH 7 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Nomenclature

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board D975XBX's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

Specification Update for the Intel® Desktop Board D975XBX



GENERAL INFORMATION

Basic Desktop Board D975XBX Identification Information

AA Revision	BIOS Revision	Notes
D27094-302	BX97510J.86A.0305	1-2
D27094-303	BX97510J.86A.0442	1-2
D27094-304	BX97510J.86A.0618	1-2
D27094-305	BX97510J.86A.1304	1-2
D27094-306	BX97510J.86A.1351	1-2
D29532-302	BX97510J.86A.0442	1-2
D29532-303	BX97510J.86A.0618	1-2
D29532-304	BX97510J.86A.1304	1-2
D29532-305	BX97510J.86A.1351	1-2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82955XE Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping
82975X MCH	A0
82801GDH ICH7	A1



Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D975XBX. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	SPECIFICATION CHANGES
1	Doc	Changes to Section 1.10.2 Audio Connectors
NO.	PLANS	ERRATA
		There is not characterized erratum for this product



SPECIFICATION CHANGES

1. *The following note will be added to the Technical Product Specification in Section 1.10.2 Audio Connectors:*



INTEGRATOR'S NOTES

Electrostatic discharge (ESD) can damage desktop board components. Front-panel connectors should provide sufficient protection to prevent ESD damage to components inside the chassis enclosure.