

Previously Logo'd Motherboard Logo Program (PLMP)

Intel® Desktop Board DH67BL

PLMP Report

3/30/2011

Purpose:

This report describes the DH67BL Previously Logo'd Motherboard Logo Program testing run conducted by Intel Corporation.

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Introduction

Terms and Definitions

Term	Definitions		
WHQL	Windows* Hardware Qualification Lab		
WLK	Windows Logo Kits		
PLMP	Previously Logo'd Motherboard Logo Program. For further information see: http://www.microsoft.com/whdc/hwtest/default.mspx		
AP Machine	Audio Precision Machine		
Winqual	Windows Qualification		
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&g=s		

Desktop Board Configuration

Desktop Board DH67BL Final Configuration Report: Completion of PLMP

Data in this section reflects system configuration at time of PLMP submission.

Board Information

Product Code ¹	BIOS String/Model	Technologies NOT Logo'd (yet)		
DH67BL	BLH6710H.86A.0062.2010.1025.1539	N/A - all technologies logo'd		
Processor				
Speed	3.40GHz			
Family	Intel Core i7 CPU 2600			
Bus Speed	100 MHz			
Motherboard				
Board AA #	G10189-201			
Board FAB #	В			
	* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.			
System Memory				
Speed	Dual Channel, DDR3, 1333MHz			
Memory Type	DIMM			
Connector Type	DDR3, 240 Pin			
Power Management				
BIOS Default	BIOS Default S3			
Operating System	Operating System Tested			
	Check Tested	Comments		
Windows 7 and 64-bit		Windows 7 Ultimate		
Windows Vista and 64-bit		Vista Ultimate with Service Pack 2		
Windows Vista Basic and 64-bit	□ Vista Basic with Service Pack 2			

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit

Technology	OS	Version	Package version	
Chipset Update Utility	Windows 7	V9.2.0.1015	INF_allOS_9.2.0.1015_ PV	
Intel [®] Chipset Software Utility	Windows 7 64-bit	V9.2.0.1015	INF_allOS_9.2.0.1015_ PV	
				L
Audio	Windows 7	V6.0.1.6215	AUD_Vista_Win7_6.0.1. 6215_PV	
Realtek High Definition Audio	Windows 7 64-bit	V6.0.1.6215	AUD_Vista_Win7_6.0.1. 6215_PV	
LAN	Windows 7	V11.8.74.0	LAN_allOS_11.8.74.0_P V	
Intel® 82579V Gigabit Network Connection	Windows 7 64-bit	V11.8.74.0	LAN_allOS_11.8.74.0_P V	
MEI	Windows 7	V7.0.0.1118	MEI_allOS_7.0.0.1135_ PV	
Intel [®] Management Engine Interface	Windows 7 64-bit	V7.0.0.1118	MEI_allOS_7.0.0.1135_ PV	

Windows Logo Kits Used (WLK)

 $\label{lem:microsoft.com/whdc/DevTools/WDK/DTM.mspx} \begin{subarray}{ll} Microsoft website: $$\underline{$http://www.microsoft.com/whdc/DevTools/WDK/DTM.mspx}$ \end{subarray}$

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit	
Windows 7	WLK1.5 for Windows 7	WLK1.5 for Windows 7	
Windows 7 64-bit	WCK1.5 for Willdows 7	WERT.5 for Williams 7	

Errata and Contingencies

Operating System	Failing Test	Expiry Date	ID Number	Туре	Error Description
Windows 7 Windows 7 64-bit	Class Driver AC3 Test - Win7 (System)	6/30/2025	1256	Erratum	Run AC3 test on a system with the Microsoft HD Audio class driver installed. Expected results: All AC3 kernel streaming data ranges should advertise MinimumBitsPerSample = 16 and MaximumBitsPerSample = 16. Actual results: HD Audio class driver sometimes advertises MaximumBitsPerSample = 24.
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	6/1/2011	401	Erratum	The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	6/1/2011	923	Erratum	Assertion FAE18121-9177-4FB2-A081-0D04C285EFF2 Bit range 15:0 (Extended Capability ID)in the Enhanced Capability Header register (offset 0h) in the Unrecognized Enhanced Capability ID 13 table is Dh. It must be in the range [0x0 - 0xB] as all other Capability IDs are reserved.
Windows 7 Windows 7 64-bit	UAA Test - Win7 (System)	6/1/2011	1300	Erratum	HD Audio pin configuration document calls out setting Port Connectivity to No Connection as the way to turn a pin off in a particular system. UAA Test incorrectly tests such pins.
Windows 7 Windows 7 64-bit	UAA Test - Win7 (System)	6/1/2015	513	Erratum	UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.
Windows 7 Windows 7 64-bit	UAA Test - Win7 (System)	2/28/2011	1299	Erratum	Preview filter - Jack Detect Override on digital pin widgets Errata 1299 The HD Audio configuration default register (7.3.3.31 in the HD Audio specification) includes a "Jack Detect Override" flag that can be used to indicate that although a pin widget would normally be capable of jack detection, there is something about this particular system that causes this to be impossible. This was intended to be used, for example, for analog pin widgets that are connected to RCA jacks, which do not allow for impedance detection. Some digital pin widgets are using the Presence Detect pin sense response to indicate that a digital handshake has occured - indeed, HDMI pins have entire DCNs built around this concept, and it applies equally well to S/PDIF pins. A digital converter that supports presence detection should be able to do so in any system, so the "Jack Detect Override" concept should not apply to digital pins.

Test Notes

Operating System	Test	Description		
Windows 7 and Vista	BIOS download	Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download		
Windows 7 and Vista	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component (Except CIR) Drive Configuration: Set to AHCI Chipset Configuration: Enable HPET ACPI Suspend State: Set to <s3 state=""> Boot Device Priority: set <hard disk="" driver=""> to first Note: Enhanced Consumer IR (CIR) component is not supported under Windows7.</hard></s3>		
Windows 7 and Vista filter update	WLK WHQL test	http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filterupdates.cab		
Special H/W that use to PASS the test	None	None		