

Previously Logo'd Motherboard Program (PLMP)

Intel[®] Desktop Board DP55WG

PLMP Report

7/23/2010

Purpose:

This report describes the DP55WG Previously Logo'd Motherboard Program testing run conducted by Intel Corporation.

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Terms and Definitions

Term	Definitions		
WHQL	Windows* Hardware Qualification Lab		
WLK	indows Logo Kits		
PLMP	Previously Logo'd Motherboard Program. For further information see: <u>http://www.microsoft.com/whdc/hwtest/default.mspx</u>		
AP Machine	Audio Precision Machine		
Winqual	Windows Qualification		
MSFT Tested Product List	Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&g=s		

Desktop Board Configuration

Desktop Board DP55WG Final Configuration Report: Completion of PLMP

Data in this section reflects system configuration at time of PLMP submission.

Board Information

Product Code ¹	BIOS String/Model	Technologies NOT Logo'd (yet)			
DP55WG	KGIBX10J.86A.4905.2010.0402.1639	N/A - all technologies logo'd			
Processor	Processor				
Speed	2.93GHz				
Family	Intel [®] Core(TM) i7-870				
Bus Speed / DMI	2.5 GT/s				
Motherboard					
Board AA #	657269				
Board FAB #	405				
	es to the production FAB revision; Please of ion you intend to perform logo testing if n	consult your Intel Corporation representative to clarify the ot the same.			
System Memory					
Speed	DUAL Channels, DDR3, 1333MHz				
Метогу Туре	DIMM				
Connector Type	DDR3,240pin				
Power Management					
BIOS Default	S3				
Operating System	Operating System Tested				
	Check Tested Comments				
Windows 7 and 64-bit		Windows 7 Ultimate			
Windows Vista and 64-bit		Vista Ultimate with Service Pack 2			
Windows Vista Basic and 64-bit	U Vista Basic with Service Pack 2				

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Technology	OS	Version	Package version
Chipset Update Utility	Windows 7	9.1.2.1007	9.1.2.1007
Intel [®] Chipset Software Utility	Windows 7 64-bit	9.1.2.1007	9.1.2.1007
Graphics	Windows 7	8.17.11.9745	197.45
Nvidia GeForce GTS 250	Windows 7 64-bit	8.17.11.9745	197.45
Audio	Windows 7	6.0.1.6106	6106
Realtek	Windows 7 64-bit	6.0.1.6106	6106
LAN	Windows 7	11.5.10.0	15.1
Intel	Windows 7 64-bit	11.5.10.0	15.1
CIR	Windows 7	2.1.2009.619	2.1.2009.0619
Nuvoton	Windows 7 64-bit	2.1.2009.619	2.1.2009.0619

Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit

Windows Logo Kits Used (WLK)

Microsoft website: <u>http://www.microsoft.com/whdc/DevTools/WDK/DTM.mspx</u>

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

Operating Systems	Notes	WHQL Testkit
Windows 7	WLK1.5 for Windows 7	WLK1.5 for Windows 7
Windows 7 64-bit		WERT.S TOF WINDOWS 7

Errata and Contingencies

Operating System	Failing Test	Expiry Date	ID Number	Туре	Error Description	
Windows 7 Windows 7 64-bit	Class Driver AC3 Test - Win7 (System)	6/30/2025	1256	Erratum	Run AC3 test on a system with the Microsoft HD Audio class driver installed. Expected results: All AC3 kernel streaming data ranges should advertise MinimumBitsPerSample = 16 and MaximumBitsPerSample = 16. Actual results: HD Audio class driver sometimes advertises MaximumBitsPerSample = 24.	
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/1/2010	401	Erratum	The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a.	
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/1/2010	423	Erratum	Assertion 5CAF4993-B8D1-4E4E-99EC-CC5895364E32 Bit range 11:10 (ASPM Support)in the Link Capabilities register (offset Ch) in the PCI Express Capability table is Oh. It cannot be in the set of values {0x0, 0x2}.	
Windows 7 Windows 7 64-bit	PCI Hardware Compliance Test For Systems	12/1/2010	1241	Erratum	This happens because the PCI Compliance test assumes that if the Data Link Layer Link Active Reporting Capable bit in the Link Capabilities register for a given PCle port is set then that indicates that the Data Link Layer Link Active bit will also be set. This is an incorrect assumption because the Data Link Link Layer Link Active bit can be reset when there is no device below the port. This assertion needs to be removed from the PCIHCT. The current architecture of the PCIHCT prevents it from knowing whether devices exist below a bridge/port.	
Windows 7 Windows 7 64-bit	UAA Test - Vista or Server08 (System)	6/1/2015	513	Erratum	UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure.	

Test Notes

Operating System	Test	Description
Windows 7	BIOS download	Internal: <u>http://bios.intel.com/downloads/</u> External: <u>http://www.intel.com/</u> click on Support and Download
Windows 7	BIOS setup	Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component Drive Configuration: Set to IDE Chipset Configuration: Enable HPET ACPI Suspend State: Set to <s3 state=""> Boot Device Priority: set <hard disk="" driver=""> to first</hard></s3>
Windows 7 filter update	WLK WHQL test	http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filte rupdates.cab
Special H/W that use to PASS the test	None	None