



Intel[®] Server Board SE7320EP2 /SE7525RP2

Specification Update

Intel Order Number D27104-003

Nov, 2006



Enterprise Platforms and Services Division – Marketing

Revision History

Date of Revision	Version	Description
Nov , 2006	001	This document is the first Specification Update for the Intel® Server Board

Disclaimers

The SE7320EP2 /SE7525RP2 may contain design defects or errors known as errata that may cause the product to deviate from the published specifications. Current characterized errata are documented in this Specification Update.

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Preface

This document is an update to the specifications contained in the *Intel® Server Board SE7320EP2 and Se7525RP2 Technical Product Specification*. It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain specification changes, specification clarifications, errata, and document changes.

Refer to the *Intel® Xeon™ Processor Specification Update* (Order Number 244460-021) for specification updates concerning the Pentium® Xeon™ processor. Items contained in the *Xeon™ Processor Specification Update* that either do not apply to the [product] or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the Printed Board Assembly (PBA) revisions(s) associated with that stepping.

Nomenclature

- **Specification Changes** are modifications to the current published specifications for Intel® server boards. These changes will be incorporated in the next release of the specifications.
- **Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.
- **Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.
- **Errata** are design defects or errors. Errata may cause the server board behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.

Nov, 2006

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Summary Tables of Changes

The following tables indicate the errata and the document changes that apply to the SE7320EP2 and SE7525RP2 boards. Intel intends to fix some of the errata in a future stepping of components, and to account for the other outstanding issues through documentation or specification changes as noted. The tables use the following notations:

Doc: Intel intends to update the appropriate documentation in a future revision.

Fix: Intel intends to fix this erratum in a future release of the component.

Fixed: This erratum has been previously fixed.

NoFix: There are no plans to fix this erratum.

Shaded: This erratum is either new or has been modified from the previous specification update.

Table1. Documentation Changes

No.	Plans	Description of Documentation Change
		None at this time

Following are in-depth descriptions of each erratum / documentation change indicated in the tables above. The errata and documentation change numbers below correspond to the numbers in the tables.

SPEC Update items

1. Intel published Marvell network driver does not SUPPORT VLAN CONFIGURATION UNDER WINDOWS.

- Problem** Customers reported that Intel published Marvell Network driver can not support VLAN configuration under Windows. Customer discovered there is some driver in-sync between Intel published driver on Intel support website and Marvell published driver on Marvell website.
- Implication** If install Intel published Marvell NIC driver, customer will not have the VLAN feature under Windows on Marvell NIC port. Basically Intel only published the Intel certified and validated driver for Marvell NIC on SE7320EP2 and SE7525RP2, which does not have VLAN feature. Meanwhile, Marvell published the latest driver for their NIC component (just for NIC chip silicon instead of for any board product)
- Workaround** None.
- Status** Intel published Marvell driver will not support VLAN feature.

Documentation Changes

None at this time