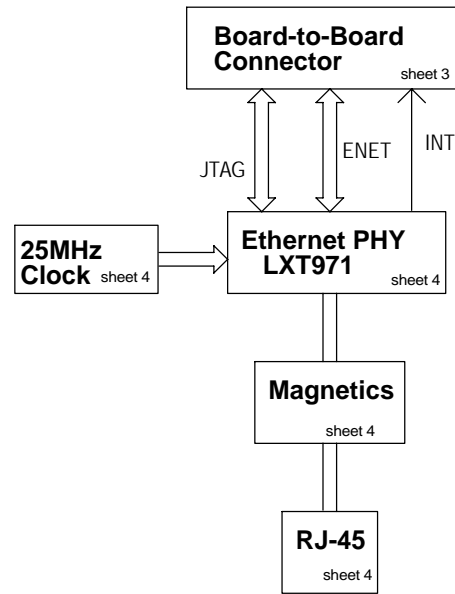



ETHERNET PHY CARD

for BIXMB425AD Network Processor Base Card



| | | |
|---|--|---|
| Intel Corp 2200 Mission College Blvd Santa Clara, CA 95052 | |  |
| Title BIXD100 ETHERNET PHY CARD | | |
| Size B | Page Title Title Page / Block Diagram | |
| Date: Monday, January 10, 2005 | Sheet 1 of 4 | |

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SCHEMATIC ANNOTATIONS

VOLTAGE RAILS

- +3V3

Generated on IXP425 baseboard. Supplied to the LXT972 and surrounding circuitry.
- +3V3_ENET

Analog power generated from +3.3V on this module. Supplied to the LXT972 and surrounding circuitry.

GROUND

- GND_DIGITAL

Digital Ground Plane

DESIGN NOTES

- 0.0 ohm resistors are placed to allow accessibility to nodes that may be of interest.
- _DNP

Do Not Populate this component if this designation is found on component's schematic.

REVISION HISTORY

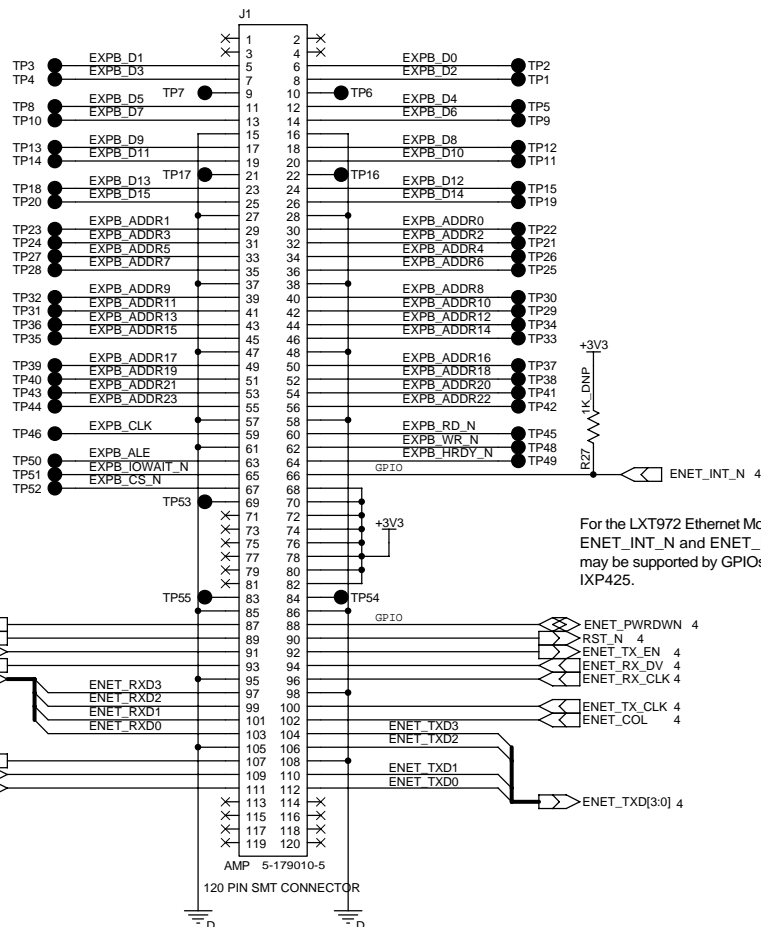
- Rev 0.97

* Schematics for prototype build
- Rev 1.1

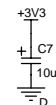
* Changed device from LXT972 to LXT971.

4 JTAG_TCK
4 JTAG_TDI
4 JTAG_TDO
4 JTAG_TMS
4 ENET_RXD[3:0]

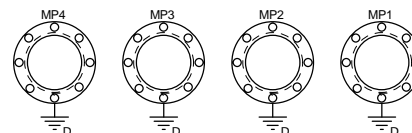
4 ENET_MDC
4 ENET_MDIO
4 ENET_CRD



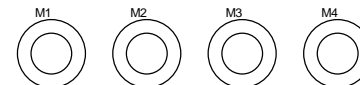
For the LXT972 Ethernet Module,
ENET_INT_N and ENET_PWRDWN
may be supported by GPIOs from the
IXP425.



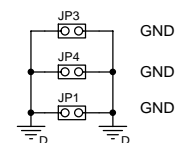
MP1-MP4 are to be placed in the four corners of the board.



M1-M4 are to be placed along the board edge.



Silkscreen:



Intel Corp

2200 Mission College Blvd
Santa Clara, CA 95052



Title BIXD100 ETHERNET PHY CARD

Size
B

Page Title

Board-To-Board Connector

Rev
1.1

Date: Monday, January 10, 2005

Sheet

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1

