intel

DATE: February 6, 2001

FROM: Intel Customer Support

SUBJECT: SU81010A.86A Standard SU810 BIOS P10

About This Release

- **BIOS String:** SU81010A.86A.0010.P10. 0101241508
- Video BIOS Version: 4.01.2102
- UNDI PXE Version: 2.0 (Build 74)

Features/Errata Fixed in This Release

P10-0010

- Fixed the functionality of the IDE UDMA mode BIOS SETUP option on the IDE Configuration page.
- Fixed the issue of 633Mhz Celeron[™] processor speed being displayed as 650Mhz
- Added support for 'Intel Express BIOS Update' version 1.1.
- Added BIOS update C0 stepping support for Pentium(R) III processor PPGA.
- Updated Intel Copyright banner.

P09-0009

- Fixed a device conflict relating to Intel 82801AA SMBus Controller found during WHQL Pre-test
- Fixed issue where UserPassword & Unattended Start settings could halt POST
- Implemented Fast-Table Driven Post
- Enabled processor support for up to 800MHz
- Enabled restore to Custom BIOS Defaults when bad CMOS check sum occurs
- Implemented Passwords Checksum fix to prevent phantom password bug
- Fixed issues with mouse detection and high-frequency processors.
- Fixed issue where Processor Speed was being set to a default state after loading defaults in Setup. This only occurred at high-processor frequencies.
- Fixed the speed displayed during POST and Setup with processors that support the extended frequencies.
- Fixed issue where the DMI function 54h sub-function 4008h SPD programming interface was not working properly.
- Fixed the problem where invalid CMOS/GPNV error occurred after disabled LPT port from Microsoft
- Windows* 95/98 APM mode system device manager.
- Added support for Simple Boot Flag specification version 1.0.
- Fixed issue where BIOS detects an option ROM on 3COM EtherLink XL PCI cards that don't have an option ROM.
- Added support for USB legacy with dual USB controllers.
- Fixed issue where a floppy seek was occurring when Quick Boot was enabled in BIOS SETUP.
- Resolved a CMOS Checksum error during POST after Custom Defaults were forced by Intel specific INT15.
- Fixed DMITEST Failures where the SMBIOS handle is incorrect for the Memory Structures.

P08-0008

- Fixed hang while trying to go into Event Log Configuration in BIOS setup in Manufacturing mode (or with Quiet Boot set to Disabled)
- Added support for 810 GMCH and 810 GMCH0 Video Bios
- Fixed issue where some single sided DIMMs fail memory stress tests.
- Fix issue of User Access Level not being displayed when Supervisor Password is installed.

- Implemented RTC fix for ICH TSMC issue.
- Fixed WOL hang with 3COM* Fast Ether Link 3C905x.
- Added support for NEC* & Toshiba* 3-mode floppy drives.
- Fixed issue of invalid PCI subsystem vendor ID for embedded PCI devices

P05-0005

- Added support for the GMCH A3 stepping.
- Added a fix for the double boot (Build 74 of the PXE code.)
- Added a fix for the PCI resource allocation issue
- Added a security fix for the problem that allows boot from the floppy when user Password and unattended start are both enabled.
- Added the IDE/Video disable fix for PC99 compliance.
- Added support for Pentium® III processors with 128KB cache.
- Added error beeps if a video adapter is not detected during post (one long beep, followed by 2 short beeps.
- Increased the maximum number of PCI busses from 16 to 256
- Modified the display cache algorithm to work with 810 and 810E platforms
- Added Processor Serial Number (PSN) enable/disable
- Added CD ROM Recovery
- Removed floppy drive from Boot Menu when Floppy Controller and Floppy Drive is not present in the system
- Added support for POST status screen which will appear just before booting the operating system if and only if the toggle key was pressed during post
- Fixed hang whenever Novell* Boot Ware PRO-100B v1.00 card is in the system
- Fixed hang with booting the Windows 98 CD on a SCSI CDROM, and selecting boot to hard drive when the hard drive is an IDE-HDD
- Fixed issue where ARMD-FDD devices would not work if the Floppy Controller was DISABLED and Floppy A Type was set to "Not Installed" in SETUP.
- Fixed booting of floppy emulated CDROMS on SCSI CDROM drives with Adaptec SCSI cards

P04-0004

- Added diagnostic LED Support
- Added support for ISA Slots
- Updated to GMCH BIOS Spec Update 1.0
- Added fix to correct the CPU sped displayed when a 533MHZ Celeron[™] processor was installed in the system
- Added fix to address the problem where the BIOS would set UDMA-66 timings for an UDMA-66 capable drive when a 40 conductor IDE cable and CDROM or hard drive was attached to the same channel
- Added fix to address the problem where Windows NT* will hang during installation when APM is set to disabled in BIOS Setup.
- Fixed problem where assigning IRQ 7 to PCI Slots does not work
- Added the support to display "E" and "EB" MHz for overlapping processor speeds.
- Fixed BBS issue where only the first SCSI drive CDROM booting on Adaptec cards.
- Fixed issues with Option ROMs with sizes >= 64KB (Creative TNT2)
- Fixed issues where system would not boot in the setup default boot order if one of the default questions
- pointed to a non-existent boot device.
- Added RPD-specific requirements
- Fixed problem where occasionally, mouse will randomly open windows coming out of Suspend.
- Fixed a problem where the logo would be corrupted and the system would hang during POST if the toggle key was pressed after POST code 89h and before POST code 98h
- Fixed issue where Date/Time is updated when CMOS Checksum is bad
- Fixed hang at checkpoint A2h if floppy drive not installed or defective.
- Fixed issue where POST reports keyboard detected with no keyboard attached.
- Fixed problem where Windows display Floppy Drive A: even when it is disabled in Setup

- Fixed a problem where the setup option for "After Power Failure" did not work in ACPI mode when it was set to "Last State" and the system was shutdown; the system would always return to "On"
- Fixed issue where the next system boot after corrupting the CMOS is prompting for the password to enter SETUP
- Added Maintenance screen option to Clear BIS Credentials.

P03-0003

• Initial Release